

Model Name: GA-EX58-DS4

1.0

SHEET

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TITLE

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52	uPI6262, CMOS, POWER, RESET BUTTON
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Gigabyte Technology

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GA-EX58-DS4

**Version: 1.0**

Circuit or PCB layout change  
for next version

## Component value change history

[illegible][illegible]

Low ICH9 GPIO LIST TABLE

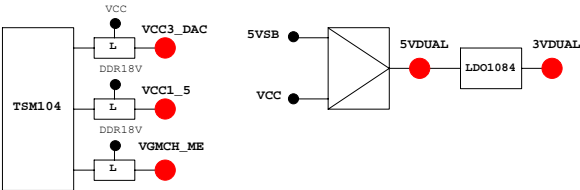
PIN NAME	PWR WELL	AFTER/BIOS	USAGE	NOTE
GP0	MAIN	IN	VTT_GMCH_OV3	
GP1/TACH1	MAIN	IN	ICH_FAN_TACH1	P/U 8.2K VCC3
GP2/PIRQE#	MAIN	IN	-PIRQE	P/U 8.2K VCC3
GP3/PIRQF#	MAIN	IN	-PIRQF	P/U 8.2K VCC3
GP4/PIRQG#	MAIN	IN	-PIRQG	P/U 8.2K VCC3
GP5/PIRQH#	MAIN	IN	-PIRQH	P/U 8.2K VCC3
GP6/TACH2	MAIN	IN	ICH_FAN_TACH2	P/U 8.2K VCC3
GP7/TACH3	MAIN	IN	ICH_FAN_TACH3	P/U 8.2K VCC3
GP8	STBY	IN	DDR18V_OV4	
GP9	STBY	H-Z	GPIO9 (DUALBIOS INPUT)	
GP10	STBY	H-Z	DDR18V_OV5	P/D 100K GND/X
GP11/SMBALERT#	STBY	NATIVE	-SMBALRT	P/U 8.2K 3VDUAL
GP12	STBY	L OUT	AUDIO DETECT	P/U 8.2K VCC3
GP13	STBY	L IN	-LPCPME	P/U 8.2K 3VDUAL
GP14	STBY	H-Z	DDR18V_OV2	P/U 8.2K 3VDUAL
GP15	STBY	H-Z	SPI_WP	STP_PCI#
GP16	MAIN	L OUT	DUAL BIOS CONTROL	N/A
GP17/TACH0	MAIN	IN	ICH_FAN_TACH0	P/U 8.2K VCC3
GP18	MAIN	H OUT	MB_ID1	P/U 8.2K VCC3
GP19	MAIN	IN	VCC15_OV1	P/U 8.2K VCC3/X
GP20	MAIN	OUT	-SPI_WF0	P/U 1K 3VCL
GP21	MAIN	IN	VCC15_OV3	P/U 8.2K VCC3
GP22	MAIN	IN	VCORE_OV3	P/U 8.2K VCC3
GP23	MAIN	OUT	-LDRQ1	P/U 8.2K VCC3
GP24	STBY	OUT	TL5	P/U 8.2K 3VDUAL
GP25	STBY	IN	MB_ID2 (STP_CPU-)	P/U 8.2K 3VDUAL
GP26/S4_STATE#	STBY	OUT	MB_ID0	P/U 8.2K 3VDUAL
GP27	STBY	OUT/LOW	GPIO27 (EL_STATE0)	P/U 8.2K 3VDUAL
GP28	STBY	OUT/LOW	DUAL BIOS CONTROL	N/A
GP29/OC5#	STBY	IN	-USBOC_R	P/U FUSEVCC
GP30/OC6#	STBY	IN	-USBOC_R	P/U FUSEVCC
GP31/OC7#	STBY	IN	-USBOC_R	P/U FUSEVCC
GP32	MAIN	OUT	DUAL BIOS	P/U 100K+1M VCC3
GP33	MAIN	OUT		
GP34	MAIN	OUT/LOW		N/A
GP35	MAIN	L OUT	400K FS CONTROL	N/A
GP36	MAIN	IN	DUAL BIOS CONTROL	P/U 8.2K VCC3
GP37	MAIN	IN	150K FS CONTROL	P/U 8.2K VCC3
GP38	MAIN	IN	VCORE_OV2	P/U 8.2K VCC3
GP39	MAIN	IN	GPIO39	P/D 8.2K GND
GP48	MAIN	IN	VCORE_OV1	P/U 8.2K VCC3
GP49	MAIN	IN	STARPPING	P/D 8.2K

PIN NAME	PWR WELL	AFTER/BIOS	USAGE	NOTE
GP50	MAIN	IN	REQ1#	
GP51	MAIN	IN	GNT1#	P/U 8.2K VCC3
GP52	MAIN	IN	REQ2#	P/U 8.2K VCC3
GP53	MAIN	IN	GNT2#	P/U 8.2K VCC3
GP54	MAIN	IN	REQ3#	P/U 8.2K VCC3
GP55	MAIN	IN	GNT3#	P/U 8.2K VCC3
GP56	STBY	IN	VCORE_OV5	
GP57	STBY	IN	VCORE_OV4	
GP58	STBY	IN	SPI_CS1#	
GP59	STBY		-USBOC_R	
GP60	STBY		LINKALRT#	

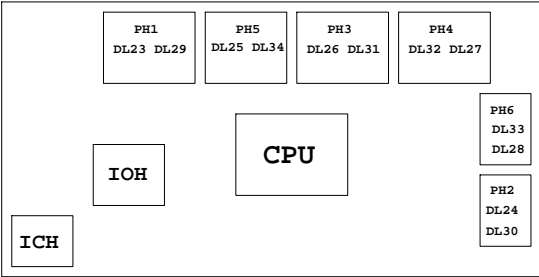
Super I/O GPIO Table

PIN NAME	USAGE	NOTE
SVC/PECI_RQT/GP14	-PECI_REQ	
PWROK1/GP13	PWROK1/ITE_PWROK	
KRST#/GP62	-KBRST	
SO/GP50	-ICH_SPI_CS	
IRTX/GP47/CE2_N/JP7	CEB_N	
GP46/IRRX	-LAN2_DSM	
PSION#/GP42	-PSON	
PWROK2#/GP41	PECI_CTL	
PCIRST3#/GP10/VDIMM_STR_EN	-PCIE_RST	
RSMRST#CIRRX1/GP55	-RSMRST	
PME#/GP54	-LPCPME	
PD5/GP75/BUSSO0	N/A	

PIN NAME	USAGE	NOTE
FAN_TAC2/GP52	FANIO2	
FAN_TAC3/GP37	FANIO3	
VIDO3/FAN_TAC4/GP25/DSR2#	FANIO4	
FAN_CTL2/GP51	FANPWM2	
FAN_CTL3/GP36	FANPWM3	
VID4/GP34	BEEP-	
VID3/GP33	TURBO1	
VID2/GP32	TURBO0	
VCORE_GOOD/VID6/GP63	CPUT_LED1_C	
VID5/GP35	CPUT_LED2_C	
VID1/GP31	CPUT_LED3_C	
VID0/GP30	-LAN1_DSM	NBT_LED1_C
SLCT/GP80	CPU_LED1_C	
PE/GP81	CPU_LED2_C	
BUSY/GP82	CPU_LED3_C	
PD3/GP73/BUSS11	SB_LED1_C	
PD4/GP74/BUSS12	SB_LED2_C	
VCORE_EN/VID7/GP64	IT_GP64	SB_LED3_C
PD0/GP70	NB_LED1_C	
PD1/GP71	NB_LED2_C	
PD2/GP72/BUSS10	NB_LED3_C	
GP22/SCK	LOW_PWR_1	
VID05/GP27/SIN2	LOW_PWR_2	
PCIRST2#/GP11	-PWRST1	
PCIRST1#/GP12	-PWRST2	
3VBSBW#/GP40	CSI_F0	BSEL166_1
SUSC#/GP53	CSI_F1	BSEL166_2
GP23/SI	BSEL166_3/CSISBSL	
VIDO0/GP20/CTS2#	CPUT_LED1_C	BSEL166_4
GP65/VDDA_EN/GB_01	MB_ID2	
PD6/GP76/BUSS01	MB_ID3	
PD7/GP77/BUSS02	MB_ID4	
AFD#/GP86/SMBC_R	2M_PIN	FST_2X8
INIT#/GP85/SMBD_M	SEC_2x8	GTLREF_AD2
ACK#/GP83	DDR_LED1_C	
VID01/GP21/DCD2#	DDR_LED2_C	
STB#/GP87/SMBC_M	DDR_LED3_C	
PWRON#GP44	VCORE_OV1	
PANSWH#/GP43	PWRBTSW	
KDAT/GP61	-PWRBTSW	
KCLK/GP60	KDAT	
MDAT/GP57	KCLK	
MACL/GP56	MDAT	
GP66/VLDT_EN/GB_02	NBT_LED1_C	MCLK
SVD/PCIRSTIN#/CIRTX/GP15	PWM2_CR	
KDAT/GP61	PWM2_CR	
GP67/CPU_PG/GB_03	EN_LOADLINE	IT_GP67/-EN_PWM2
SLIN#/GP84/SMBD_R	-EN_PWM2	
PSI_L/FAN_CLT5/CIRRX2/GP16	-THERM	
VID04/GP26/SOUT2	DDR18V_PH2_EN	
VIDO2/FAN_TAC5/GP24/DSR2#	DDR18V_LED	
VIDO6/GP17/RI2#	1_1V_PH_EN	
VIDO7/JP6/DTR2#	JP6	
PD5/GP75/BUSS00	SB_LED3_C	



PWM各相位的擺法如下：

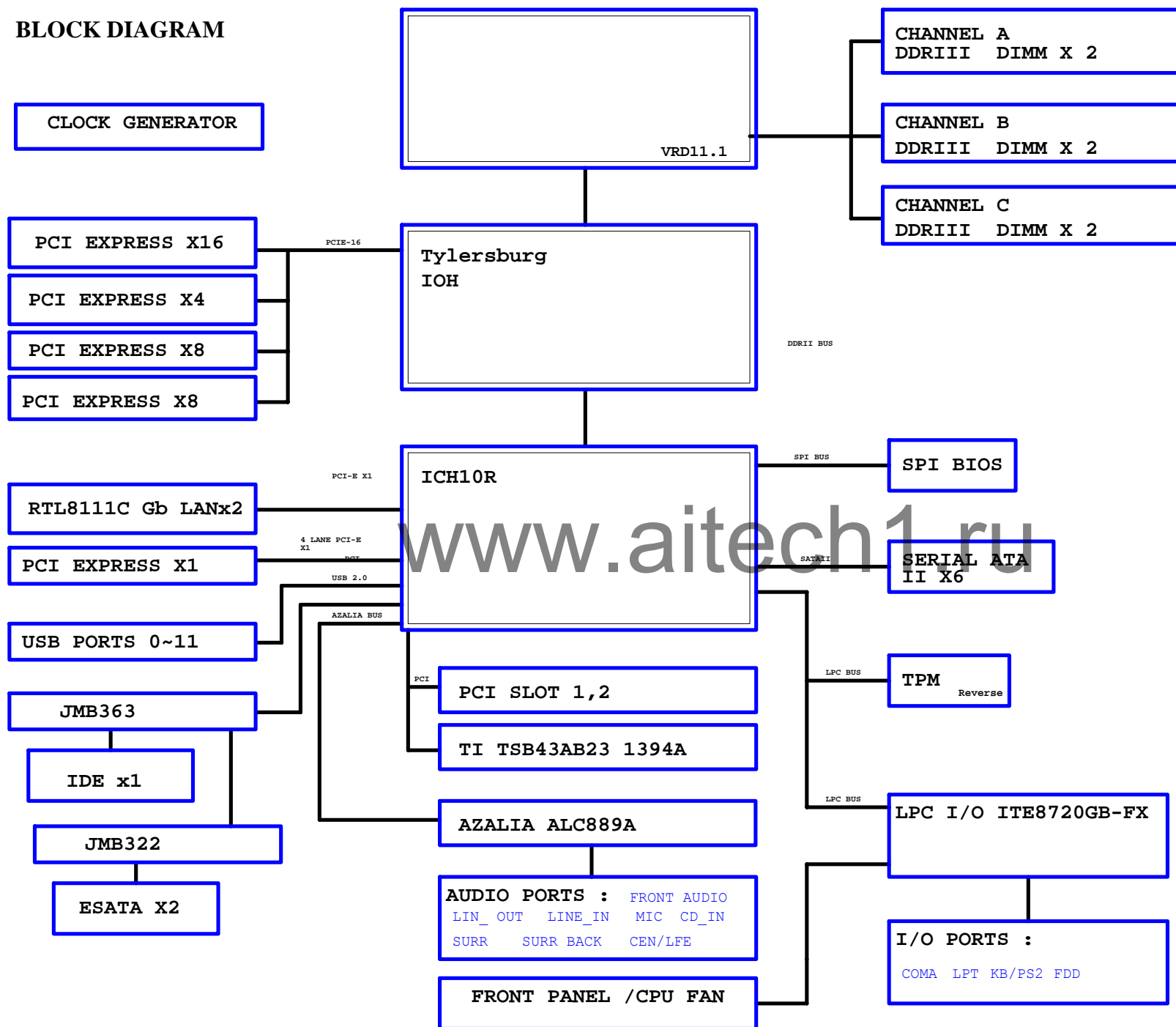


BIOS超電壓對應表：

線路圖名稱	BIOS選項
VTT_REF	DRAM Termination
CHAC-CHCC	address
DDR15V	DRAM Voltage
VCC18_PLL	CPU PLL
VCCA1_1	CSI PLL
CPU Vcore	CPU Vcore
VCC15	ICH I/O
VCC1_1	IOH core
MCH_RAMVREF	MCH/DRAM Reference(不開放)
VTTD	CPU Termination
VCCA1_5	PCIE
CHA-CHC	Date
VCC1_1_ICH	ICH core

	3 pin FAN control	4 pin FAN control	FAN speed	Controller
CPU FAN	FANPWM1	FANPWM3	FANIO1	IT8718
	ICH_FAN_PWM2	ICH_FAN_PWM0	ICH_FAN_TACH0	ICH8
SYS FAN	FANPWM2	N/A	FANIO2	IT8718
	ICH_FAN_PWM1	N/A	ICH_FAN_TACH1	ICH8
PWR FAN	N/A	N/A	FANIO3	IT8718
			ICH_FAN_TACH2	ICH8

## BLOCK DIAGRAM



LGA1366A

(18) DCLKA3 < E20  
(18) -DCLKA3 < E18  
(18) DCLKA2 < E18  
(18) DCLKA1 < E18  
(18) DCLKA0 < E18  
(18) DCLKA1 < E19  
(18) DCLKA0 < E19  
(18) -DCLKA0 < E19

(18) -CSA5 < C5A5  
(18) -CSA4 < C5A4  
(18) -CSA1 < C5A1  
(18) -CSA0 < C5A0

(18) -SRA5 < A15  
(18) -SRA4 < A15  
(18) -SWEA < A15

(18) SBA2 < B2A2  
(18) SBA1 < B2A1  
(18) SBA0 < B2A0

(18) CKEA3 < B3A3  
(18) CKEA2 < B3A2  
(18) CKEA1 < B3A1  
(18) CKEA0 < B3A0

(18) MAA15 < B29  
(18) MAA14 < B28  
(18) MAA13 < A10  
(18) MAA12 < A26  
(18) MAA11 < A26  
(18) MAA10 < B19  
(18) MAA9 < A26  
(18) MAA8 < A26  
(18) MAA7 < A26  
(18) MAA6 < C24  
(18) MAA5 < B24  
(18) MAA4 < B23  
(18) MAA3 < D24  
(18) MAA2 < C23  
(18) MAA1 < B21  
(18) MAA0 < B21

(18) B20  
(18) B33  
(18) A27  
(18) B26  
(18) D26

(18) DOSA0 < T43  
(18) -DOSA0 < U43  
(18) DOSA1 < L41  
(18) -DOSA1 < M41  
(18) DOSA2 < F41  
(18) -DOSA2 < G41  
(18) DOSA3 < B40  
(18) -DOSA3 < B40  
(18) DOSA4 < E3  
(18) -DOSA4 < E3  
(18) DOSA5 < K2  
(18) -DOSA5 < K2  
(18) DOSA6 < R2  
(18) -DOSA6 < R2  
(18) DOSA7 < W2  
(18) -DOSA7 < W1  
(18) DOSA8 < D34  
(18) -DOSA8 < D35

(18) V43  
(18) V42  
(18) M43  
(18) M42  
(18) G43  
(18) D39  
(18) C39  
(18) D5  
(18) D4  
(18) J2  
(18) J1  
(18) F7  
(18) V2  
(18) V3  
(18) B36  
(18) B35

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CPU SK 1366P/SMD/BLACK MOLEX[10SC1-F01366-31R]

ILM[12KRC-010001-11R\_12KRC-010001-12R\_12KRC-010001-13R]

BACKPLAT[12KRC-040009-11R\_12KRC-040009-12R\_12KRC-040009-13R]

LGA1366B

(19) DCLKB3 < H18  
(19) -DCLKB3 < H18  
(19) DCLKB2 < H18  
(19) DCLKB1 < H18  
(19) DCLKB0 < H18  
(19) -DCLKB0 < H18

(19) -CSB5 < C5B5  
(19) -CSB4 < C5B4  
(19) -CSB1 < C5B1  
(19) -CSB0 < C5B0

(19) -SRASB < A15  
(19) -SRASB < A15  
(19) -SWEB < A15

(19) SBAB2 < B2B2  
(19) SBAB1 < B2B1  
(19) SBAB0 < B2B0

(19) CKEB3 < B3B3  
(19) CKEB2 < B3B2  
(19) CKEB1 < B3B1  
(19) CKEB0 < B3B0

(19) MAB15 < B29  
(19) MAB14 < B28  
(19) MAB13 < A10  
(19) MAB12 < A26  
(19) MAB11 < A26  
(19) MAB10 < B19  
(19) MAB9 < A26  
(19) MAB8 < A26  
(19) MAB7 < A26  
(19) MAB6 < C24  
(19) MAB5 < B24  
(19) MAB4 < B23  
(19) MAB3 < D24  
(19) MAB2 < C23  
(19) MAB1 < B21  
(19) MAB0 < B21

(19) B20  
(19) B33  
(19) A27  
(19) B26  
(19) D26

(19) DOSB0 < T43  
(19) -DOSB0 < U43  
(19) DOSB1 < L41  
(19) -DOSB1 < M41  
(19) DOSB2 < F41  
(19) -DOSB2 < G41  
(19) DOSB3 < B40  
(19) -DOSB3 < B40  
(19) DOSB4 < E3  
(19) -DOSB4 < E3  
(19) DOSB5 < K2  
(19) -DOSB5 < K2  
(19) DOSB6 < R2  
(19) -DOSB6 < R2  
(19) DOSB7 < W2  
(19) -DOSB7 < W1  
(19) DOSB8 < D34  
(19) -DOSB8 < D35

(19) V43  
(19) V42  
(19) M43  
(19) M42  
(19) G43  
(19) D39  
(19) C39  
(19) D5  
(19) D4  
(19) J2  
(19) J1  
(19) F7  
(19) V2  
(19) V3  
(19) B36  
(19) B35

CPU SK 1366P/SMD/BLACK MOLEX[10SC1-F01366-31R]

DDR1\_COMP1 R3873 24.9/4/1

DDR1\_COMP1 R3873 24.9/4/1

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DDR1\_COMP1 R3873 24.9/4/1

DDR1\_COMP1 R3873 24.9/4/1

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DDR1\_COMP1 R3873 24.9/4/1

DDR1\_COMP1 R3873 24.9/4/1

DDR1\_COMP1 R3873 24.9/4/1

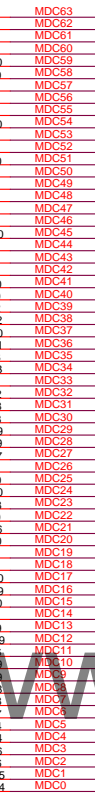
DDR1\_COMP1 R3873 24.9/4/1

DDR1\_COMP1 R3873 24.9/4/1

DDR1\_COMP1 R3873 24.9/4/1

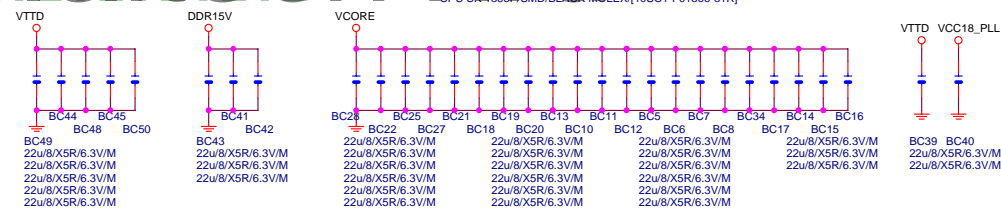
DDR1\_COMP1 R3873 24.9/4/1

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File		
LGA1366-A CPU_DDR4_B		
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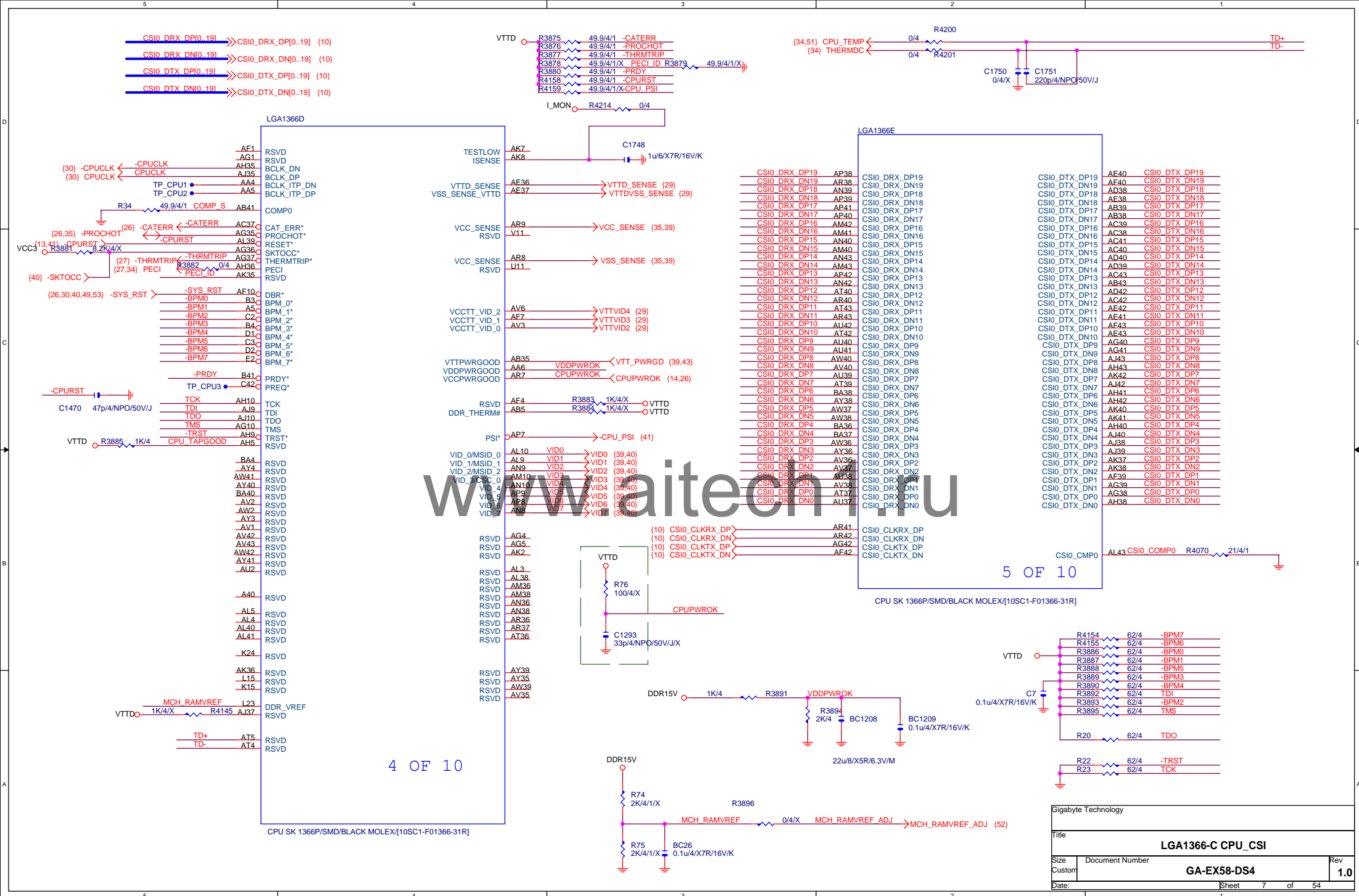
CPU下電容 Vcore 24顆  
DDR15V 3 顆  
VTTD 5 顆  
VCC18\_PLL 1 顆  
VTTA 1 顆



BOTTOM 預留

CPU下電容 Vcore 24顆  
DDR15V 3 顆  
VTTD 5 顆  
VCC18\_PLL 1 顆  
VTTA 1 顆

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Title			
LGA1366-B CPU_DDRC			
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LGA1366I

B42	VSS	AV23	VSS
B37	VSS	AV22	VSS
B2	VSS	AV20	VSS
A41	VSS	AV17	VSS
A39	VSS	AV14	VSS
A35	VSS	AV11	VSS
A6	VSS	AV4	VSS
A4	VSS	AU43	VSS
C5	VSS	AU36	VSS
E6	VSS	AU35	VSS
E1	VSS	AU32	VSS
D43	VSS	AU29	VSS
D38	VSS	AU26	VSS
D33	VSS	AU23	VSS
D8	VSS	AU22	VSS
D3	VSS	AU20	VSS
C43	VSS	AU17	VSS
C40	VSS	AU11	VSS
C35	VSS	AU14	VSS
E36	VSS	AU5	VSS
F41	VSS	AU23	VSS
F4	VSS	AT41	VSS
F9	VSS	AT38	VSS
F29	VSS	AT35	VSS
F34	VSS	AT32	VSS
F39	VSS	AT29	VSS
G2	VSS	AT26	VSS
G7	VSS	AT23	VSS
G12	VSS	AT22	VSS
G32	VSS	AT20	VSS
G37	VSS	AT17	VSS
G42	VSS	AT14	VSS
H5	VSS	AT11	VSS
H10	VSS	AT8	VSS
H30	VSS	AT7	VSS
H35	VSS	AK39	VSS
BA39	VSS	AK23	VSS
BA35	VSS	AK35	VSS
BA29	VSS	AK32	VSS
BA26	VSS	AK29	VSS
BA20	VSS	AK26	VSS
BA17	VSS	AK23	VSS
BA14	VSS	AK22	VSS
BA11	VSS	AK20	VSS
BA5	VSS	AK17	VSS
BA3	VSS	AK14	VSS
AY42	VSS	AK10	VSS
AY37	VSS	AK9	VSS
AY29	VSS	AK3	VSS
AY26	VSS	AJ41	VSS
AY23	VSS	AJ36	VSS
AY32	VSS	AJ34	VSS
AY22	VSS	AJ33	VSS
AY20	VSS	AJ5	VSS
AY17	VSS	AH39	VSS
AY14	VSS	AH37	VSS
AY11	VSS	AH34	VSS
AY7	VSS	AH7	VSS
AY2	VSS	AH1	VSS
AW35	VSS	AG43	VSS
AW32	VSS	AG33	VSS
AW29	VSS	AG11	VSS
AW26	VSS	AG9	VSS
AW23	VSS	AG3	VSS
AW22	VSS	AF41	VSS
AW20	VSS	AF38	VSS
AW17	VSS	AF35	VSS
AW14	VSS	AF5	VSS
AW11	VSS	AE39	VSS
AW8	VSS	AE7	VSS
AW6	VSS	AE2	VSS
AW1	VSS	AD43	VSS
AV41	VSS	AD41	VSS
AV39	VSS	AD37	VSS
AV32	VSS	AD33	VSS
AV29	VSS	AD11	VSS
AV26	VSS	AC36	VSS
		AC26	VSS
		AC7	VSS
		AC5	VSS
		AC2	VSS
		AN17	VSS
		AN14	VSS
		AN11	VSS

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CPU SK 1366P/SMD/BLACK MOLEX[10SC1-F01366-31R]

LGA1366J

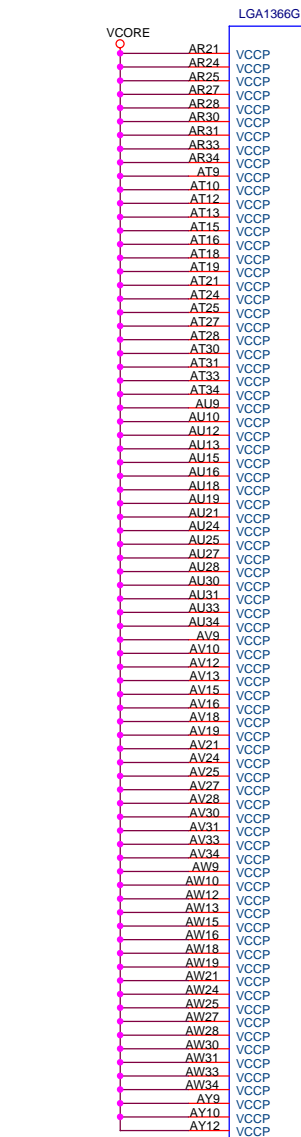
AN7	VSS	AB40	VSS
AN3	VSS	AB37	VSS
AM39	VSS	AB7	VSS
AM37	VSS	AB4	VSS
AM35	VSS	AA39	VSS
AM32	VSS	AA38	VSS
AM17	VSS	AA34	VSS
AM26	VSS	AA9	VSS
AM23	VSS	AA3	VSS
AM22	VSS	Y41	VSS
AM20	VSS	Y36	VSS
AM17	VSS	Y33	VSS
AM14	VSS	Y11	VSS
AM11	VSS	Y6	VSS
AM9	VSS	Y1	VSS
AM5	VSS	W43	VSS
AL42	VSS	W38	VSS
AL37	VSS	W8	VSS
AL36	VSS	W3	VSS
AL35	VSS	V40	VSS
AL32	VSS	V35	VSS
AL29	VSS	V10	VSS
AL26	VSS	V5	VSS
AL23	VSS	U42	VSS
AL22	VSS	U37	VSS
AL20	VSS	U7	VSS
AL17	VSS	U2	VSS
AL14	VSS	T39	VSS
AL11	VSS	T34	VSS
AL7	VSS	T9	VSS
AL2	VSS	T4	VSS
AL1	VSS	R41	VSS
AK43	VSS	R36	VSS
AK39	VSS	R6	VSS
AK34	VSS	R1	VSS
AK32	VSS	P43	VSS
AK29	VSS	P38	VSS
AK26	VSS	P33	VSS
AK23	VSS	P11	VSS
AK22	VSS	P8	VSS
AK20	VSS	P3	VSS
AK17	VSS	N40	VSS
AK14	VSS	N35	VSS
AK10	VSS	N10	VSS
AK9	VSS	N5	VSS
AK3	VSS	M42	VSS
AJ41	VSS	M37	VSS
AJ36	VSS	M32	VSS
AJ34	VSS	M30	VSS
AJ33	VSS	M28	VSS
AJ5	VSS	M26	VSS
AH39	VSS	M24	VSS
AH37	VSS	M22	VSS
AH34	VSS	M20	VSS
AH7	VSS	M18	VSS
AH1	VSS	M16	VSS
AG43	VSS	M14	VSS
AG33	VSS	M12	VSS
AG11	VSS	M7	VSS
AG9	VSS	M2	VSS
AG3	VSS	L39	VSS
AF41	VSS	L34	VSS
AF38	VSS	L29	VSS
AF35	VSS	L9	VSS
AF5	VSS	L4	VSS
AE39	VSS	K41	VSS
AE7	VSS	K36	VSS
AE2	VSS	K31	VSS
AD43	VSS	K11	VSS
AD41	VSS	K6	VSS
AD37	VSS	K1	VSS
AD33	VSS	J43	VSS
AD11	VSS	J38	VSS
AC36	VSS	J33	VSS
AC26	VSS	J13	VSS
AC7	VSS	J8	VSS
AC5	VSS	J3	VSS
AC2	VSS	H40	VSS
AN17	VSS		
AN14	VSS		
AN11	VSS		

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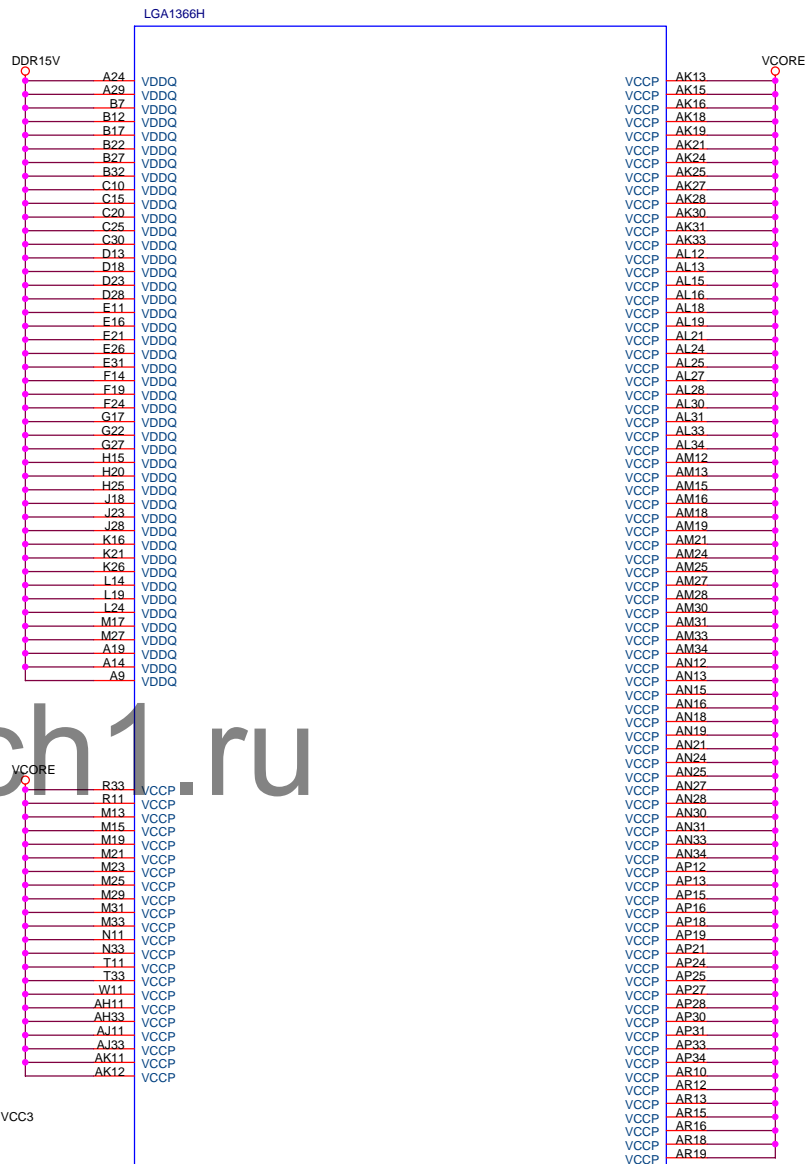
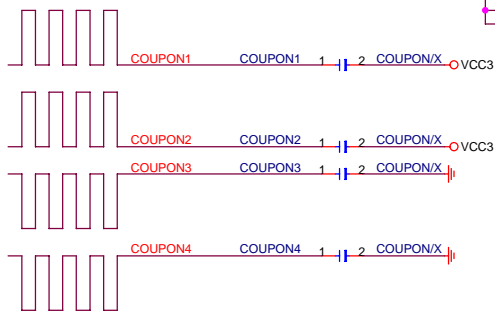
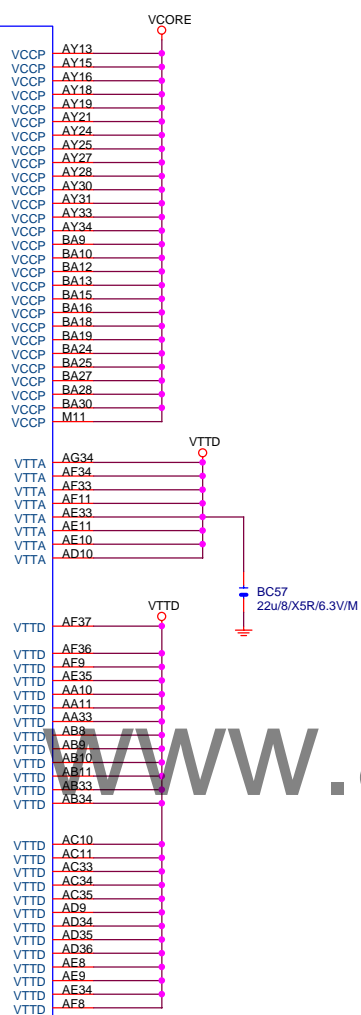
CPU SK 1366P/SMD/BLACK MOLEX[10SC1-F01366-31R]

Gigabyte Technology			
Title			
LGA1366-D GND			
Size	Document Number	Rev	
Custom	GA-EX58-DS4	1.0	
Date:	Sheet	8	of 54





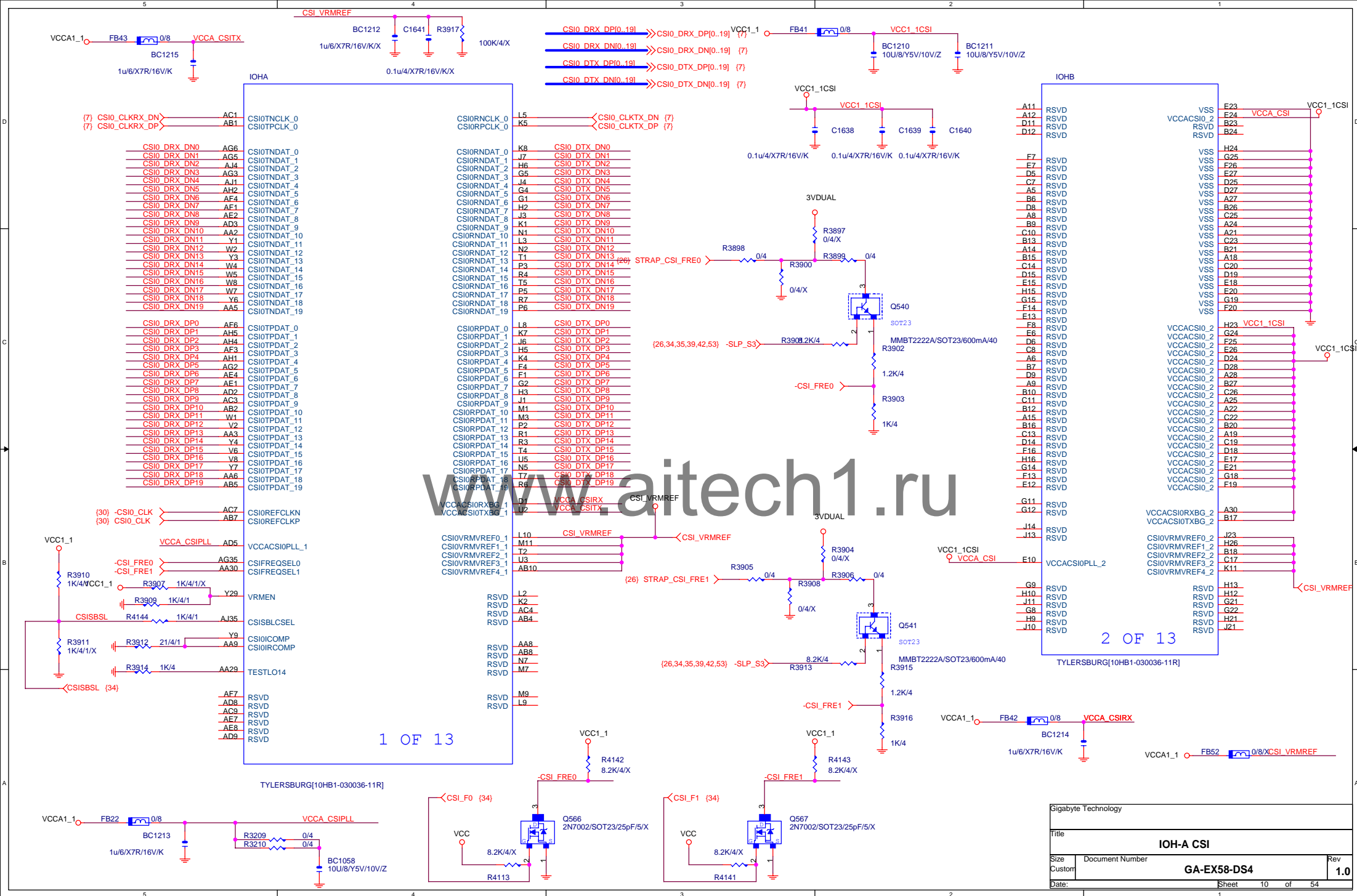
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CPU SK 1366P/SMD/BLACK MOLEX[10SC1-F01366-31R]

Gigabyte Technology		
Title		
LGA1366-E CPU_PWR		
Size	Document Number	Rev
Custom	GA-EX58-DS4	1.0
Date:	Sheet	9 of 54

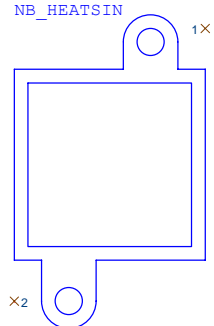
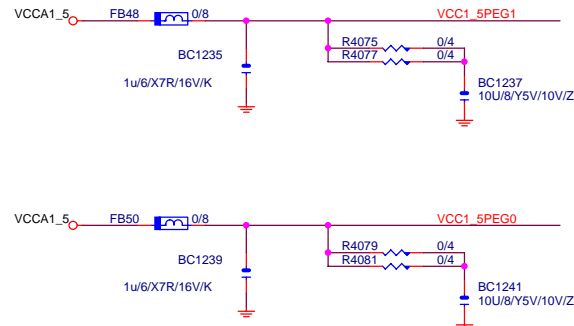
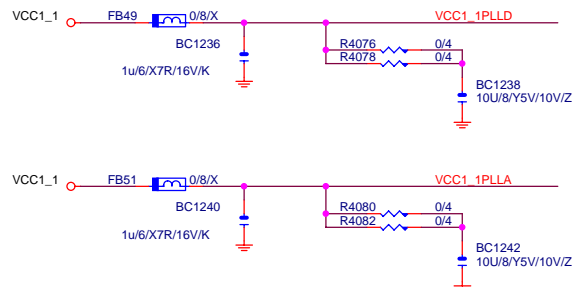
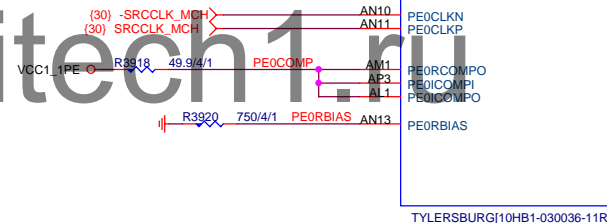


EXP A TXP[0..7] >>> EXP\_A\_TXP[0..7] (22)  
EXP A TXN[0..7] >>> EXP\_A\_TXN[0..7] (22)  
EXP A RXP[0..7] >>> EXP\_A\_RXP[0..7] (22)  
EXP A RXN[0..7] >>> EXP\_A\_RXN[0..7] (22)

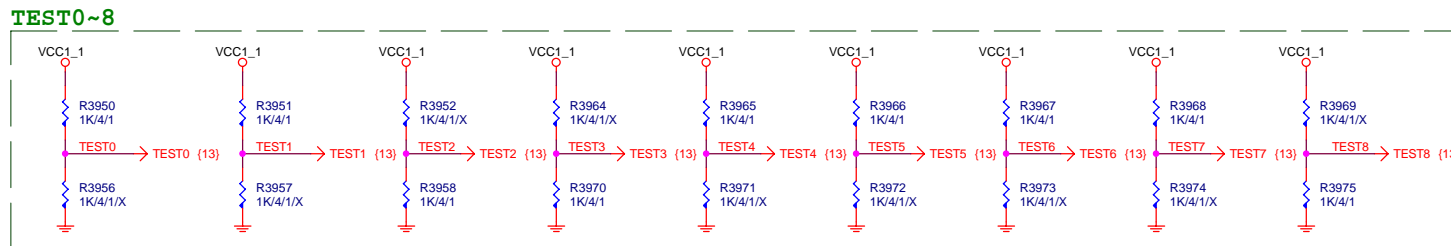
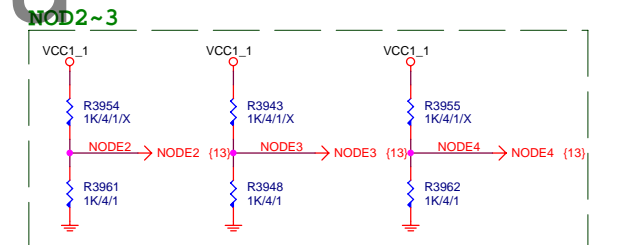
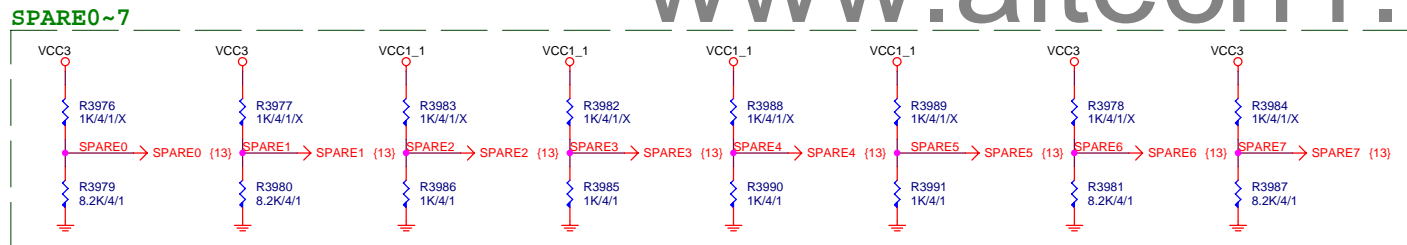
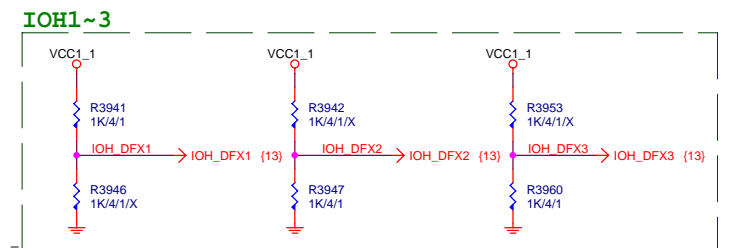
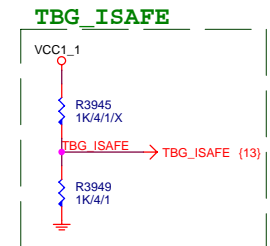
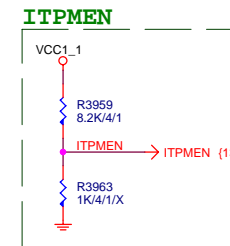
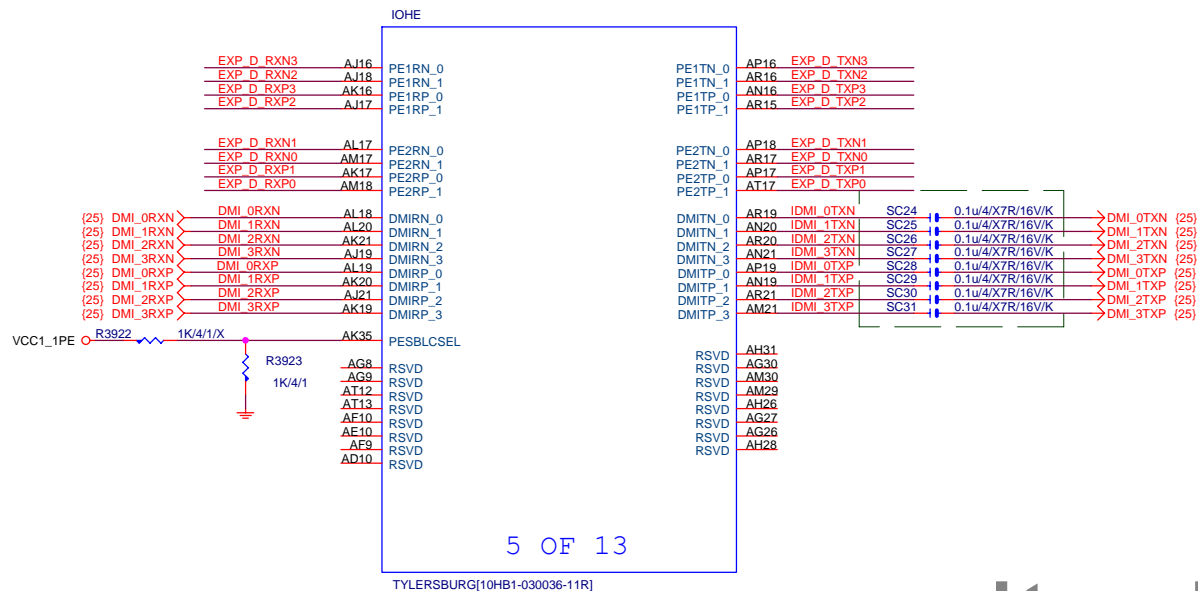
EXP A TXP[8..15] >>> EXP\_A\_TXP[8..15] (22)  
EXP A TXN[8..15] >>> EXP\_A\_TXN[8..15] (22)  
EXP A RXP[8..15] >>> EXP\_A\_RXP[8..15] (22)  
EXP A RXN[8..15] >>> EXP\_A\_RXN[8..15] (22)

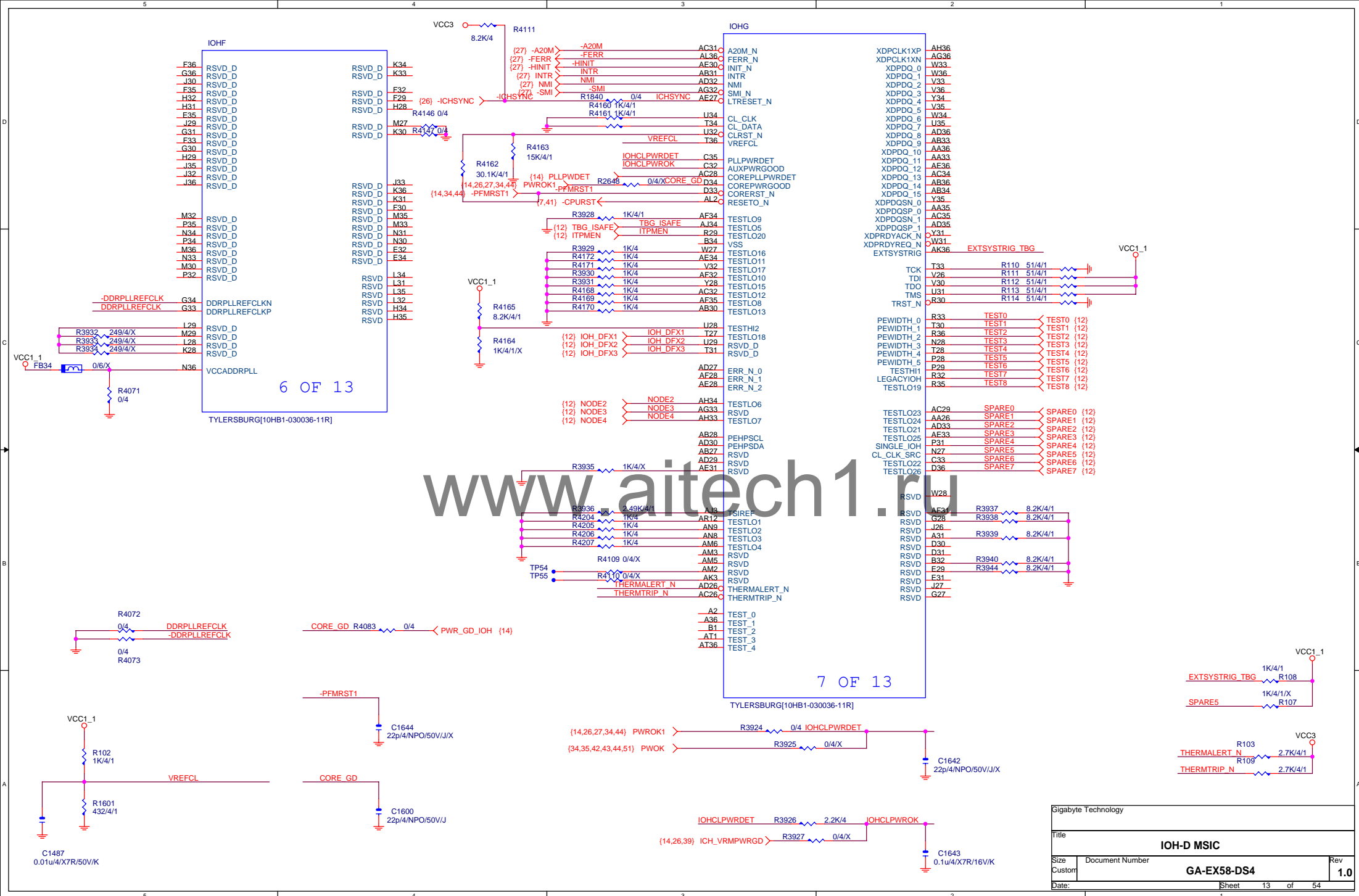
EXP C TXP[0..7] >>> EXP\_C\_TXP[0..7] (24)  
EXP C TXN[0..7] >>> EXP\_C\_TXN[0..7] (24)  
EXP C RXP[0..7] >>> EXP\_C\_RXP[0..7] (24)  
EXP C RXN[0..7] >>> EXP\_C\_RXN[0..7] (24)

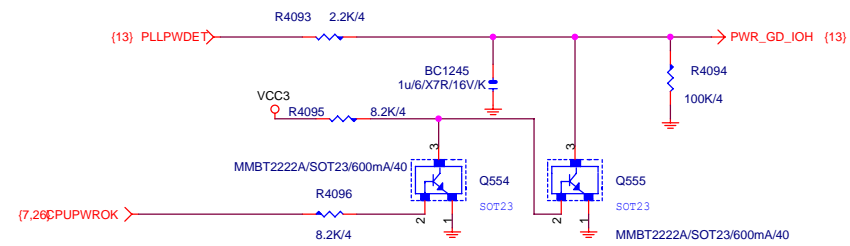
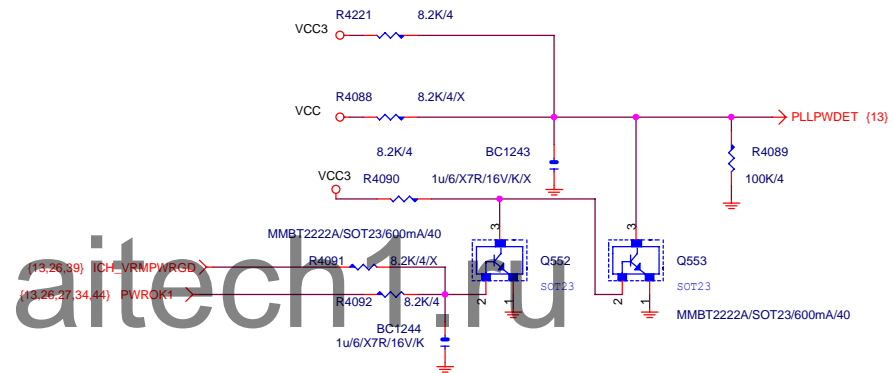
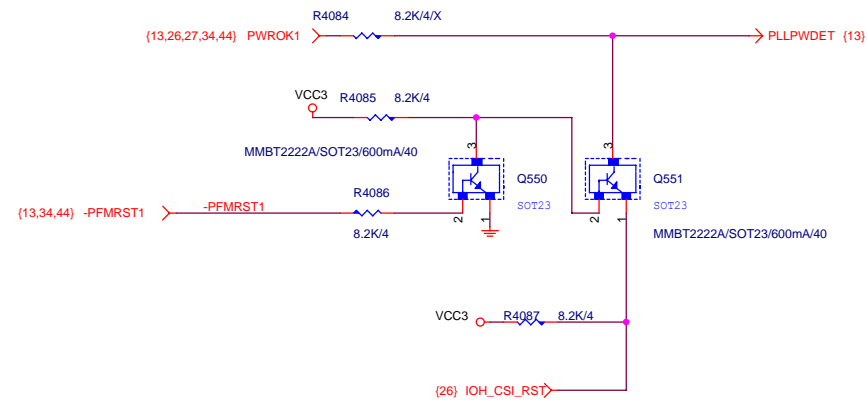
EXP C TXP[8..15] >>> EXP\_C\_TXP[8..15] (23)  
EXP C TXN[8..15] >>> EXP\_C\_TXN[8..15] (23)  
EXP C RXP[8..15] >>> EXP\_C\_RXP[8..15] (23)  
EXP C RXN[8..15] >>> EXP\_C\_RXN[8..15] (23)



Gigabyte Technology			
Title			
IOH-B PCIEX16			
Size	Document Number	Rev	
Custom	GA-EX58-DS4	1.0	
Date:	Sheet	11	of 54

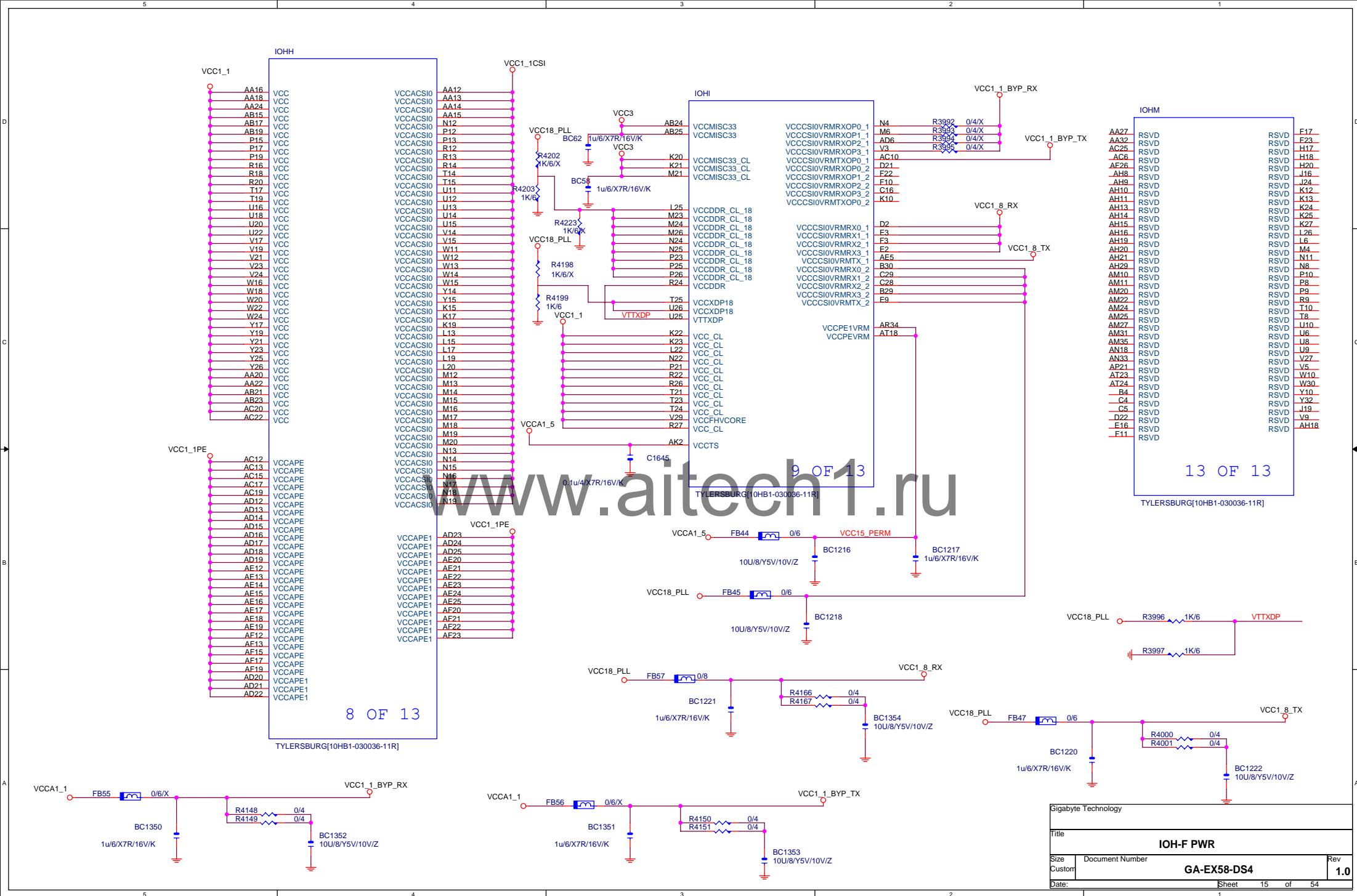






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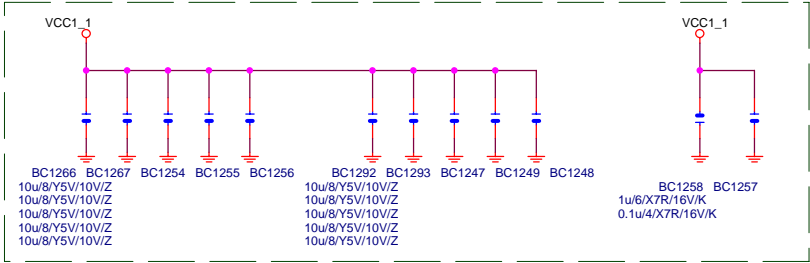
Gigabyte Technology			
Title			
IOH-E_MISC_STRAP			
Size	Document Number		Rev
Custom	GA-EX58-DS4		1.0
Date:	Sheet 14 of 54		



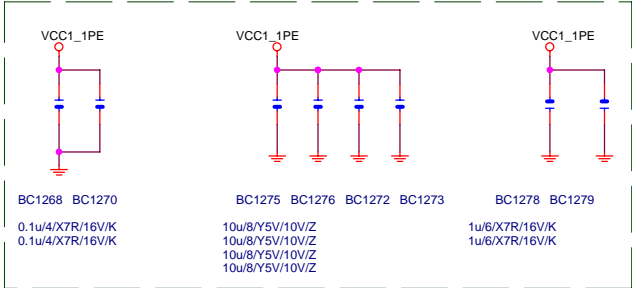


TOP side

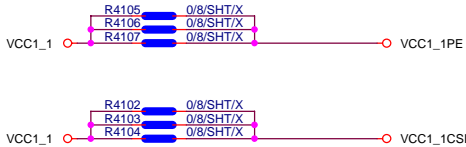
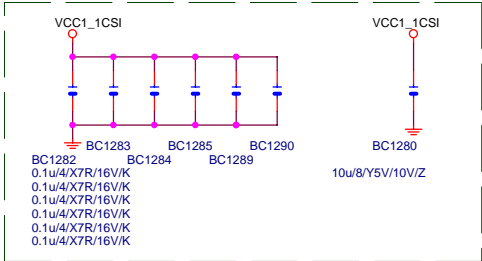
VCC1\_1



VCC1\_1PE

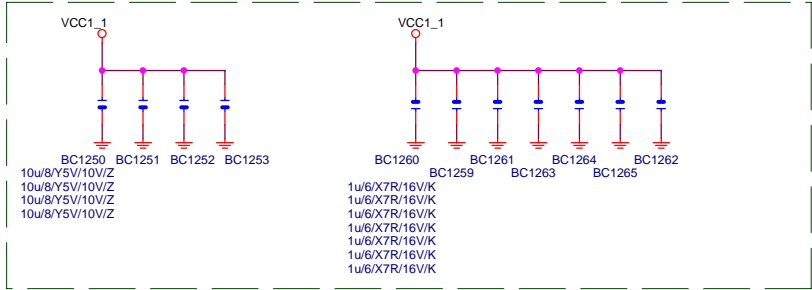


VCC1\_1CSI

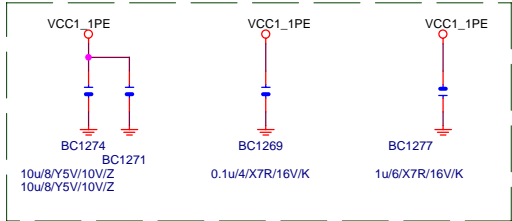


Bottom Side

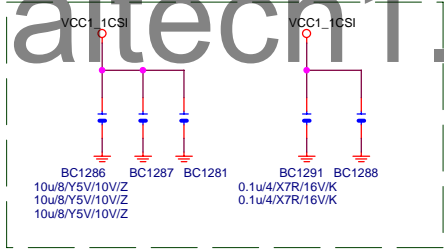
VCC1\_1



VCC1\_1PE



VCC1\_1CSI



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## IOHJ

<del>A16</del>	RSVD_SP	VSS	<del>AM9</del>
<del>A23</del>	RSVD_SP	VSS	<del>AM14</del>
<del>B14</del>	RSVD_SP	VSS	<del>AM23</del>
<del>C12</del>	RSVD_SP	VSS	<del>AM32</del>
<del>C18</del>	RSVD_SP	VSS	<del>AM36</del>
<del>D16</del>	RSVD_SP	VSS	<del>AN17</del>
<del>F12</del>	RSVD_SP	VSS	<del>AN26</del>
<del>G13</del>	RSVD_SP	VSS	<del>AN31</del>
<del>G17</del>	RSVD_SP	VSS	<del>AP1</del>
<del>H14</del>	RSVD_SP	VSS	<del>AP15</del>
		VSS	<del>AP22</del>
		VSS	<del>AP29</del>
		VSS	<del>AP36</del>
A7	VSS	VSS	<del>AR1</del>
A10	VSS	VSS	<del>AR3</del>
A29	VSS	VSS	<del>AR8</del>
A32	VSS	VSS	<del>AR18</del>
A34	VSS	VSS	<del>AR22</del>
AA4	VSS	VSS	<del>AR35</del>
AA10	VSS	VSS	<del>AT2</del>
AA17	VSS	VSS	<del>AT11</del>
AA21	VSS	VSS	<del>AT25</del>
AA25	VSS	VSS	<del>AT30</del>
AA31	VSS	VSS	<del>AT34</del>
AB3	VSS	VSS	<del>B2</del>
AB9	VSS	VSS	<del>B3</del>
AB11	VSS	VSS	<del>B8</del>
AB13	VSS	VSS	<del>B25</del>
AB16	VSS	VSS	<del>B33</del>
AB20	VSS	VSS	<del>B36</del>
AB26	VSS	VSS	<del>C6</del>
AB35	VSS	VSS	<del>C21</del>
AC8	VSS	VSS	<del>C27</del>
AC14	VSS	VSS	<del>C30</del>
AC18	VSS	VSS	<del>C34</del>
AC21	VSS	VSS	<del>D3</del>
AC24	VSS	VSS	<del>D7</del>
AC30	VSS	VSS	<del>D10</del>
AC36	VSS	VSS	<del>D20</del>
AD4	VSS	VSS	<del>D26</del>
AD11	VSS	VSS	<del>D35</del>
AD31	VSS	VSS	<del>E5</del>
AE6	VSS	VSS	<del>E11</del>
AE11	VSS	VSS	<del>E19</del>
AE29	VSS	VSS	<del>E25</del>
AE32	VSS	VSS	<del>E30</del>
AF2	VSS	VSS	<del>E36</del>
AF8	VSS	VSS	<del>F5</del>
AF11	VSS	VSS	<del>F9</del>
AF16	VSS	VSS	<del>F18</del>
AF25	VSS	VSS	<del>F21</del>
AF30	VSS	VSS	<del>F27</del>
AF36	VSS	VSS	<del>F31</del>
AG7	VSS	VSS	<del>G6</del>
AG10	VSS	VSS	<del>G23</del>
AG12	VSS	VSS	<del>G29</del>
AG14	VSS	VSS	<del>G32</del>
AG16	VSS	VSS	<del>H1</del>
AG18	VSS	VSS	<del>H4</del>
AG20	VSS	VSS	<del>H8</del>
AG22	VSS	VSS	<del>H22</del>
AG24	VSS	VSS	<del>H27</del>
AG28	VSS	VSS	<del>H33</del>
AG34	VSS	VSS	
AH6	VSS	VSS	
AH17	VSS	VSS	
AH27	VSS	VSS	
AH32	VSS	VSS	
AJ2	VSS	VSS	
AJ5	VSS	VSS	
AJ10	VSS	VSS	
AJ22	VSS	VSS	
AJ29	VSS	VSS	
AJ33	VSS	VSS	
AK4	VSS	VSS	
AK13	VSS	VSS	
AK22	VSS	VSS	
AK27	VSS	VSS	
AL11	VSS	VSS	
AL21	VSS	VSS	
AL25	VSS	VSS	
AL35	VSS	VSS	

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TYLERSBURG[10HB1-030036-11R]

## IOHK

<del>J15</del>	RSVD_SP	VSS	<del>R2</del>
<del>J18</del>	RSVD_SP	VSS	<del>R5</del>
		VSS	<del>R8</del>
J2	VSS	VSS	<del>R21</del>
J5	VSS	VSS	<del>R23</del>
J8	VSS	VSS	<del>R25</del>
J9	VSS	VSS	<del>R28</del>
J20	VSS	VSS	<del>R31</del>
J22	VSS	VSS	<del>R34</del>
J25	VSS	VSS	<del>T3</del>
J28	VSS	VSS	<del>T6</del>
J31	VSS	VSS	<del>T9</del>
J34	VSS	VSS	<del>T11</del>
K3	VSS	VSS	<del>T12</del>
K6	VSS	VSS	<del>T13</del>
K9	VSS	VSS	<del>T16</del>
K14	VSS	VSS	<del>T18</del>
K16	VSS	VSS	<del>T20</del>
K18	VSS	VSS	<del>T22</del>
K26	VSS	VSS	<del>T26</del>
K29	VSS	VSS	<del>T29</del>
K32	VSS	VSS	<del>T32</del>
K35	VSS	VSS	<del>T35</del>
L1	VSS	VSS	<del>U1</del>
L4	VSS	VSS	<del>U4</del>
L7	VSS	VSS	<del>U7</del>
L11	VSS	VSS	<del>U17</del>
L12	VSS	VSS	<del>U19</del>
L14	VSS	VSS	<del>U21</del>
L16	VSS	VSS	<del>U23</del>
L18	VSS	VSS	<del>U24</del>
L21	VSS	VSS	<del>U27</del>
L23	VSS	VSS	<del>U30</del>
L24	VSS	VSS	<del>U33</del>
L27	VSS	VSS	<del>U36</del>
L30	VSS	VSS	<del>V1</del>
L33	VSS	VSS	<del>V4</del>
L36	VSS	VSS	<del>V7</del>
M2	VSS	VSS	<del>V10</del>
M5	VSS	VSS	<del>V11</del>
M8	VSS	VSS	<del>V12</del>
M10	VSS	VSS	<del>V13</del>
M25	VSS	VSS	<del>V16</del>
M28	VSS	VSS	<del>V18</del>
M31	VSS	VSS	<del>V20</del>
M34	VSS	VSS	<del>V22</del>
N3	VSS	VSS	<del>V25</del>
N6	VSS	VSS	<del>V28</del>
N9	VSS	VSS	<del>V34</del>
N10	VSS	VSS	<del>W3</del>
N20	VSS	VSS	<del>W6</del>
N21	VSS	VSS	<del>W9</del>
N23	VSS	VSS	<del>W17</del>
N26	VSS	VSS	<del>W19</del>
N29	VSS	VSS	<del>W21</del>
N32	VSS	VSS	<del>W23</del>
N35	VSS	VSS	<del>W25</del>
P1	VSS	VSS	<del>W26</del>
P4	VSS	VSS	<del>W29</del>
P7	VSS	VSS	<del>W32</del>
P11	VSS	VSS	<del>W35</del>
P14	VSS	VSS	<del>Y2</del>
P16	VSS	VSS	<del>Y5</del>
P18	VSS	VSS	<del>Y11</del>
P20	VSS	VSS	<del>Y12</del>
P22	VSS	VSS	<del>Y13</del>
P24	VSS	VSS	<del>Y16</del>
P27	VSS	VSS	<del>Y18</del>
P30	VSS	VSS	<del>Y20</del>
P33	VSS	VSS	<del>Y22</del>
P36	VSS	VSS	<del>Y24</del>
R10	VSS	VSS	<del>Y27</del>
R11	VSS	VSS	<del>Y30</del>
R15	VSS	VSS	<del>Y33</del>
R17	VSS	VSS	<del>Y36</del>
R19	VSS	VSS	

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TYLERSBURG[10HB1-030036-11R]

## IOHL

<del>A13</del>	RSVD_SP	VSS	<del>AM4</del>
<del>A17</del>	RSVD_SP	VSS	<del>AM19</del>
<del>A20</del>	RSVD_SP	VSS	<del>AM28</del>
<del>C15</del>	RSVD_SP	VSS	<del>AM33</del>
<del>D13</del>	RSVD_SP	VSS	<del>AN3</del>
<del>D17</del>	RSVD_SP	VSS	<del>AN7</del>
<del>E14</del>	RSVD_SP	VSS	<del>AN12</del>
<del>F15</del>	RSVD_SP	VSS	<del>AP5</del>
<del>G16</del>	RSVD_SP	VSS	<del>AP10</del>
<del>J12</del>	RSVD_SP	VSS	<del>AP20</del>
<del>J17</del>	RSVD_SP	VSS	<del>AP24</del>
		VSS	<del>AP34</del>
A3	VSS	VSS	<del>AR2</del>
A4	VSS	VSS	<del>AR13</del>
A33	VSS	VSS	<del>AR27</del>
A35	VSS	VSS	<del>AR32</del>
AA1	VSS	VSS	<del>AR36</del>
AA7	VSS	VSS	<del>AT3</del>
AA11	VSS	VSS	<del>AT6</del>
AA19	VSS	VSS	<del>AT16</del>
AA23	VSS	VSS	<del>AT21</del>
AA28	VSS	VSS	<del>AT33</del>
AA34	VSS	VSS	<del>AT35</del>
AB6	VSS	VSS	<del>B5</del>
AB12	VSS	VSS	<del>B11</del>
AB14	VSS	VSS	<del>B19</del>
AB18	VSS	VSS	<del>B22</del>
AB22	VSS	VSS	<del>B28</del>
AB29	VSS	VSS	<del>B31</del>
AB32	VSS	VSS	<del>B35</del>
AC2	VSS	VSS	<del>C1</del>
AC5	VSS	VSS	<del>C2</del>
AC11	VSS	VSS	<del>C3</del>
AC16	VSS	VSS	<del>C9</del>
AC23	VSS	VSS	<del>C24</del>
AC27	VSS	VSS	<del>C31</del>
AC33	VSS	VSS	<del>C36</del>
AD1	VSS	VSS	<del>D4</del>
AD7	VSS	VSS	<del>D23</del>
AD28	VSS	VSS	<del>D29</del>
AD34	VSS	VSS	<del>D32</del>
AE3	VSS	VSS	<del>E1</del>
AE9	VSS	VSS	<del>E4</del>
AE26	VSS	VSS	<del>E8</del>
AE35	VSS	VSS	<del>E22</del>
AF5	VSS	VSS	<del>E28</del>
AF14	VSS	VSS	<del>E33</del>
AF18	VSS	VSS	<del>F2</del>
AF24	VSS	VSS	<del>F6</del>
AF27	VSS	VSS	<del>F24</del>
AF33	VSS	VSS	<del>F28</del>
AG1	VSS	VSS	<del>F34</del>
AG4	VSS	VSS	<del>G3</del>
AG11	VSS	VSS	<del>G7</del>
AG13	VSS	VSS	<del>G10</del>
AG15	VSS	VSS	<del>G20</del>
AG17	VSS	VSS	<del>G26</del>
AG19	VSS	VSS	<del>G35</del>
AG21	VSS	VSS	<del>H7</del>
AG23	VSS	VSS	<del>H11</del>
AG25	VSS	VSS	<del>H19</del>
AG31	VSS	VSS	<del>H25</del>
AH3	VSS	VSS	<del>H30</del>
AH7	VSS	VSS	<del>H36</del>
AH12	VSS	VSS	<del>Y8</del>
AH22	VSS	VSS	
AH35	VSS	VSS	
AJ15	VSS	VSS	
AJ20	VSS	VSS	
AJ24	VSS	VSS	
AJ31	VSS	VSS	
AJ36	VSS	VSS	
AK1	VSS	VSS	
AK8	VSS	VSS	
AK18	VSS	VSS	
AK26	VSS	VSS	
AK34	VSS	VSS	
AL6	VSS	VSS	
AL16	VSS	VSS	
AL22	VSS	VSS	
AL30	VSS	VSS	

TYLERSBURG[10HB1-030036-11R]

Gigabyte Technology

Title

IOH-H GND

Size  
Custom

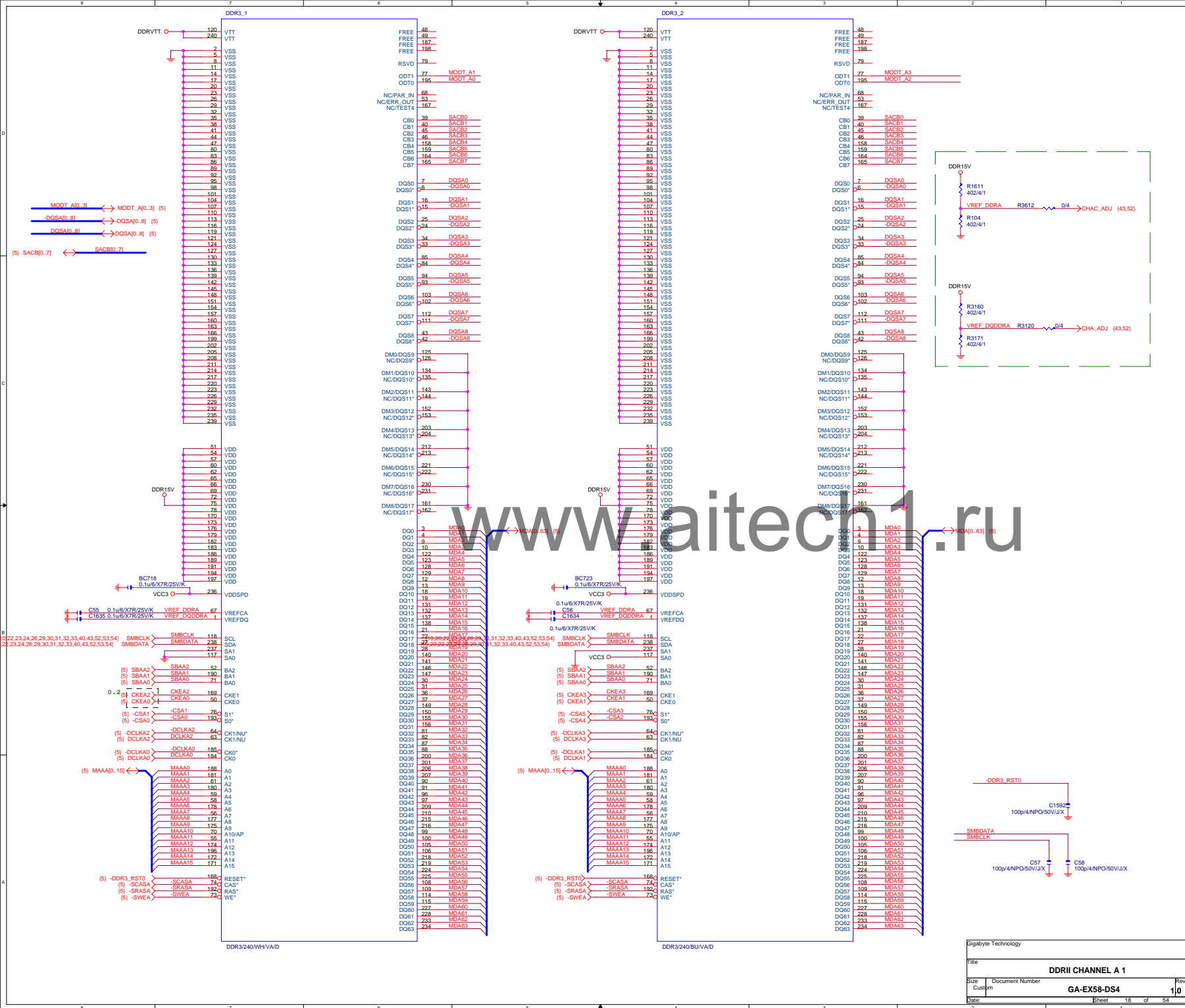
Document Number

GA-EX58-DS4

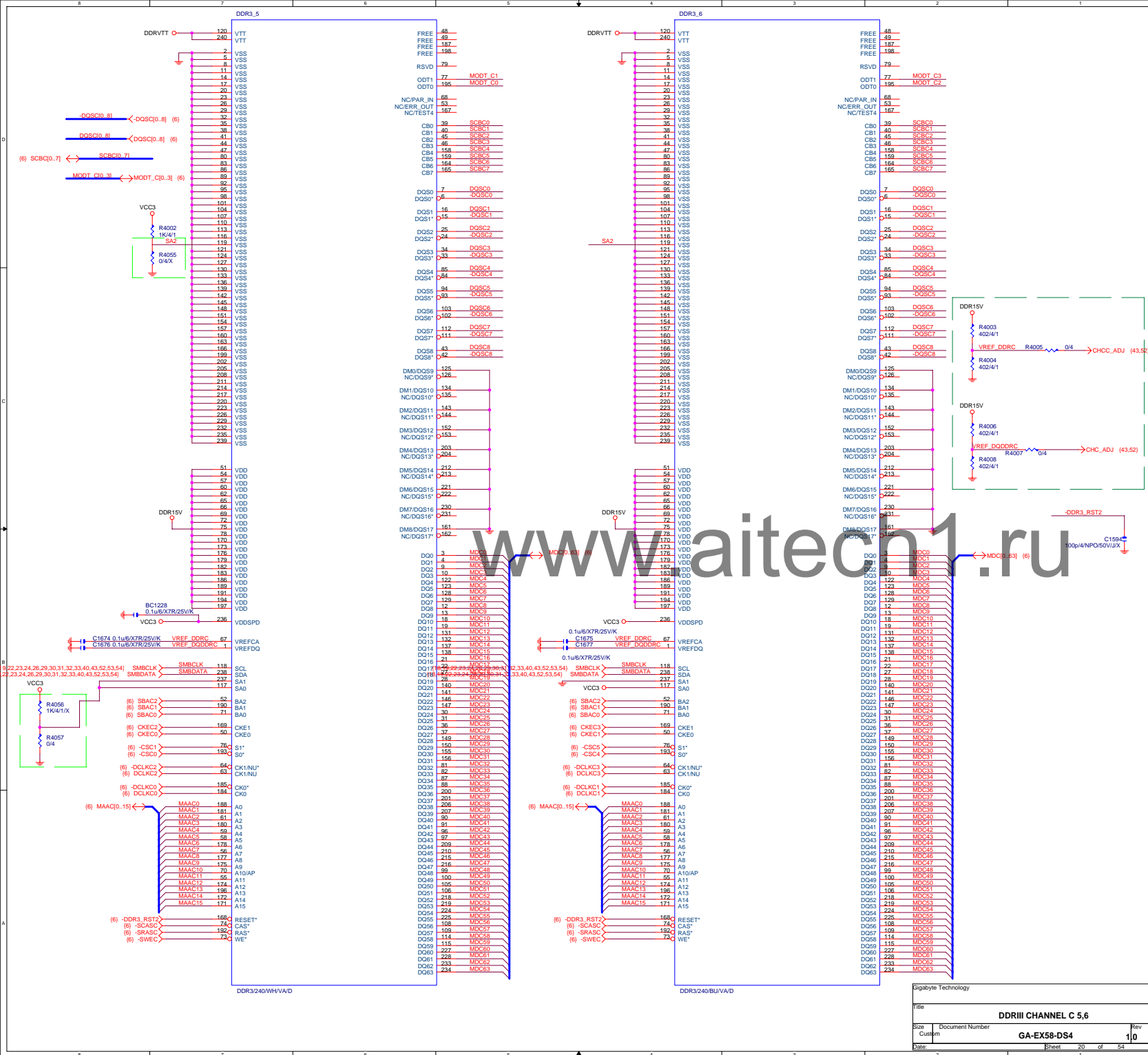
Rev  
1.0

Date:

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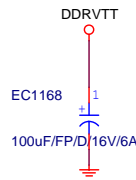
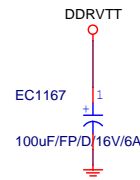
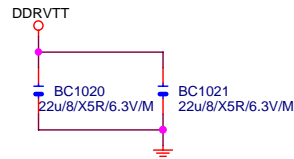
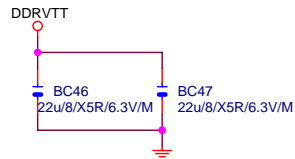
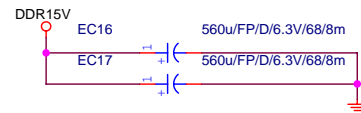
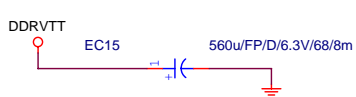






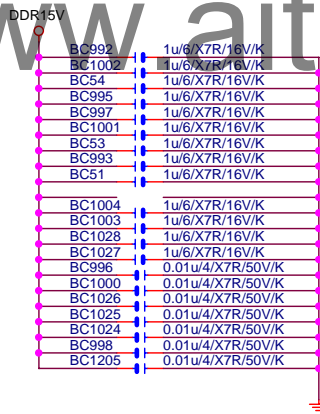
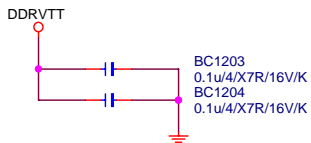
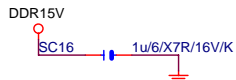
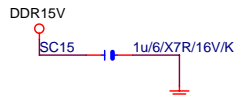
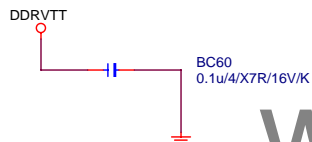
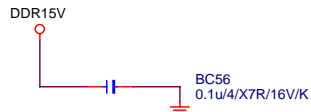
# DDR TERMINATION CHANNEL A

## DDRVTT Decouple



## DDR18V Decouple

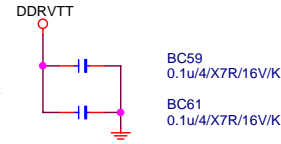
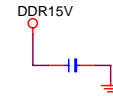
## DDRVTT Decouple



# DDR TERMINATION CHANNEL B

## DDR18V Decouple

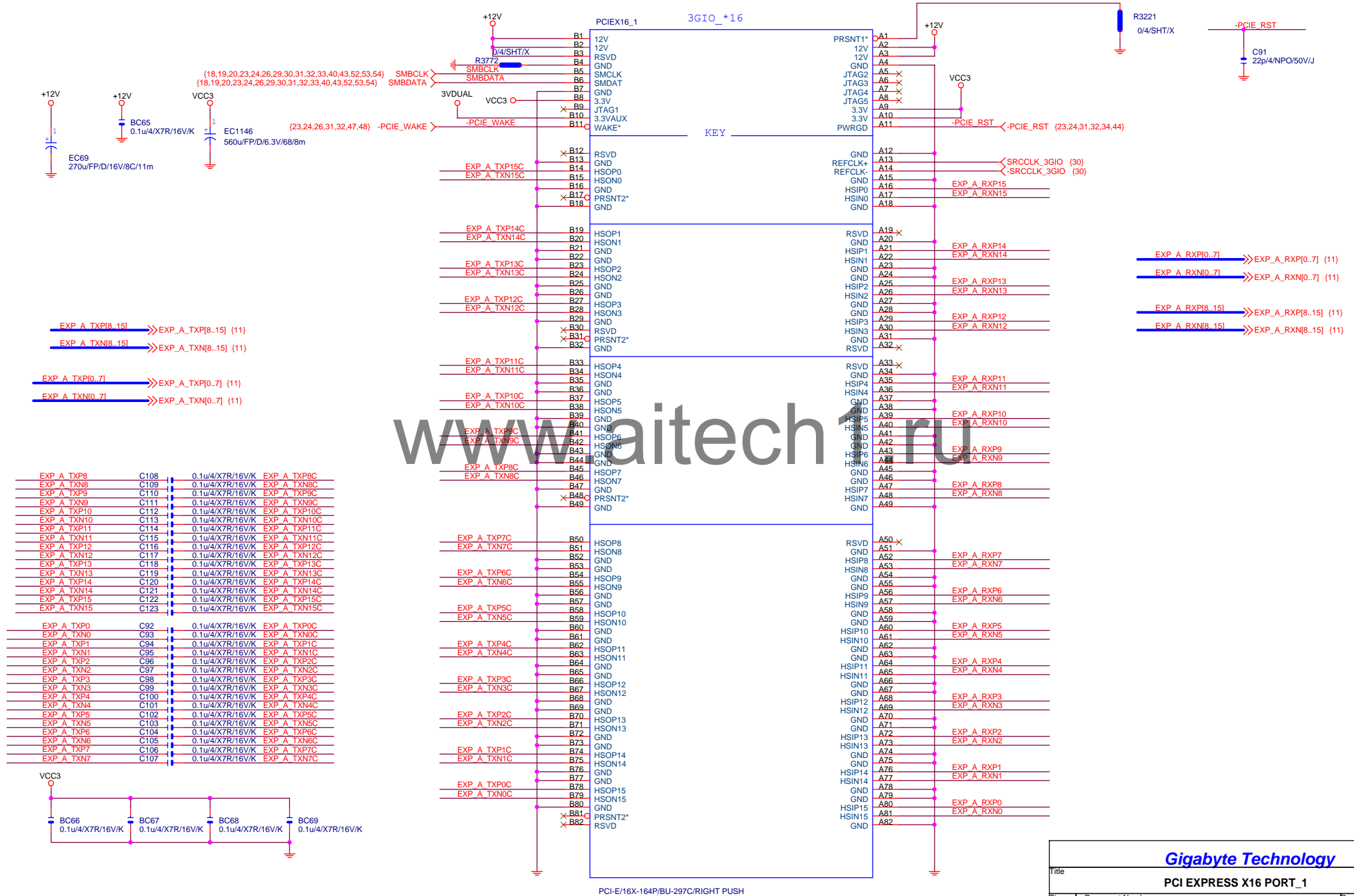
## DDRVTT Decouple



**Gigabyte Technology**

Title	DDRII TERMINATOR		
Size	Document Number	GA-EX58-DS4	Rev 1.0
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## PCIESLOT-164DN-2

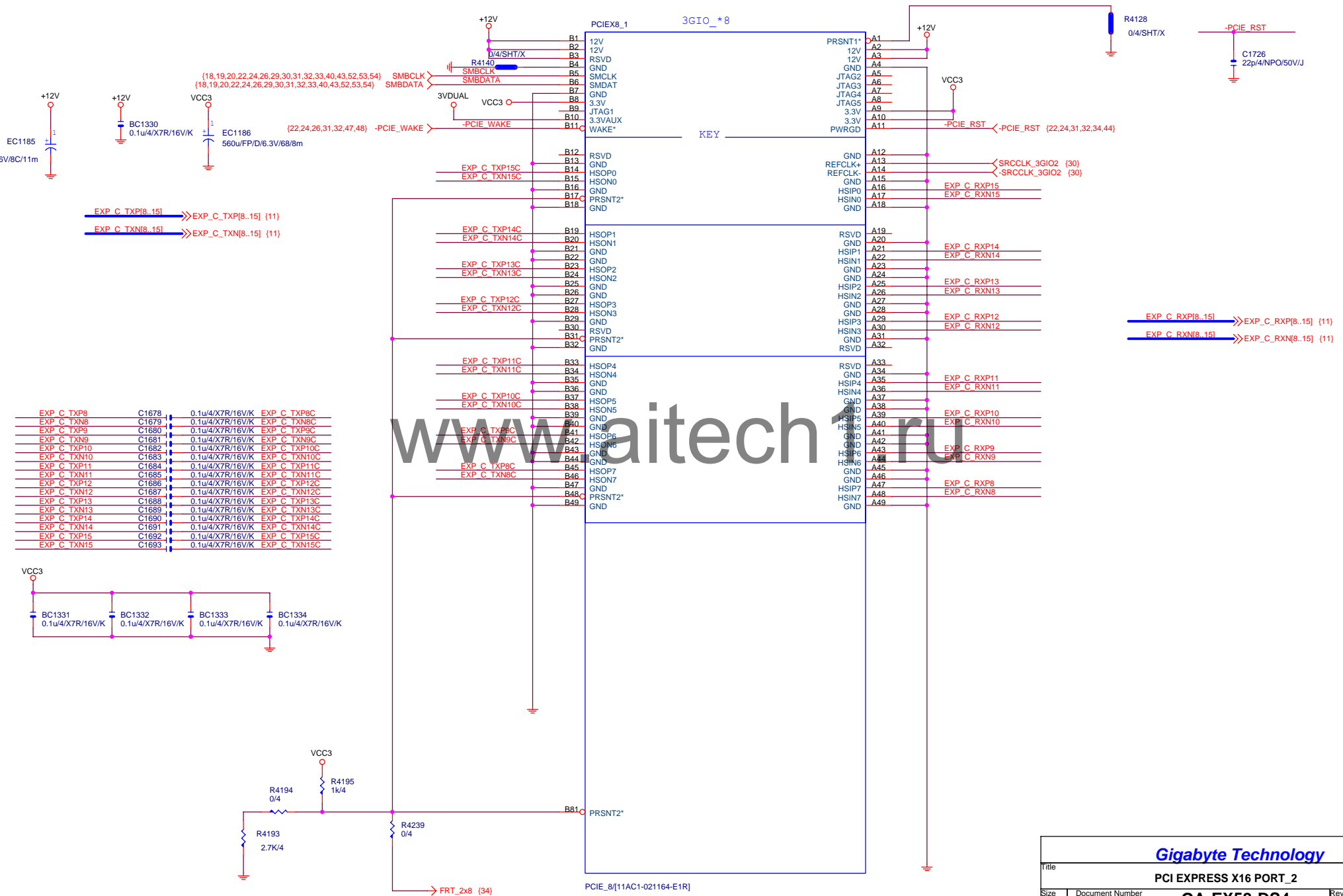


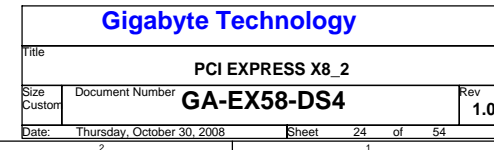
PCI-E/16X-164P/BU-297C/RIGHT PUS

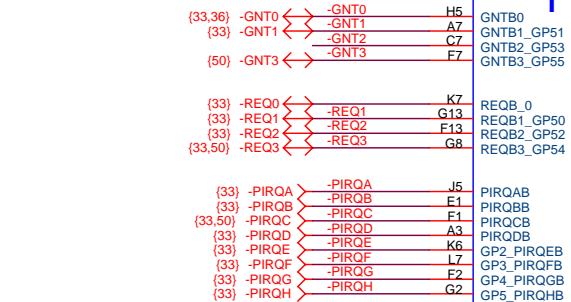
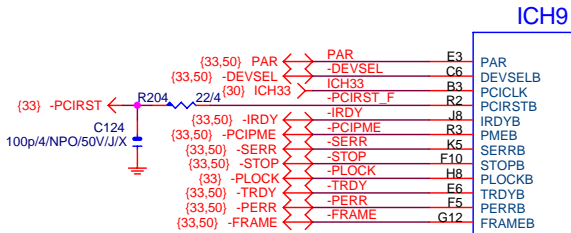
Gigabyte Technology

Title			PCI EXPRESS X16 PORT_1
Size			GA-EX58-DS4
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Date:	Thursday, October 30, 2008	Sheet	22 of 54







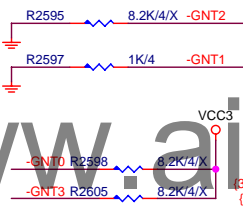


## PCI

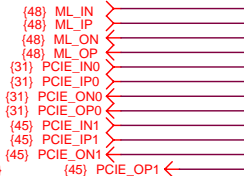
ICH10R[10HB1-038280-F0R]

## ICH GPIO Table

PIN NAME	USAGE	NOTE
GP9_WOL_EN(GPIO9)	8268_P18	
GP20(GPIO20)	8268_P18	
GP0	-PECI_REQ	
GP8	STRAP_CSI_FRE1	
GP12	STRAP_CSI_FRE0	
GP27_QRT_STATE0	3VDUAL_ICH	原ISOLATEB_1
GP26_S4_STATEB	3VDUAL_ICH	原ISOLATEB_2
CLGPIO5_GP57	F_LED1_C	
GP1_TACH1	F_LED2_C	
GP22_SCLOCK	F_LED3_C	
GP28_SLOAD	F_LED4_C	
GP21_SATA0GP	F_LED5_C	
GP6_TACH2	NBT_LED2_C	
GP39_SDATAOUT0	-CPU_PSI_DIS	
GP34(GPIO34)	-SPI_WP0	
GP48_SDATAOUT1	-EN_PWM	
GP19_SATA1GP	-ACZ_DET	
GP25	-CPU_STOP	
GP36_SATA2GP	GPIO36(FS)	
GP37_SATA3GP	SATA3GP	
SMBALERTB_GP11	-SMBALRT	
GP10_ALERTB	-CATERR	原-LAN1_DSM
GP13	-LPCPME	



PIN NAME	USAGE	NOTE
GP14_CLGPIO2(GPI14)	-ICH_PSI	
SATACLKREQB_GP35	FS_1	
GP56	IOH_CSI_RST	
GP16(GPIO16)	H/W Reset	
GP18	MB_ID1	
GP24_CLGPIO0	TLS	
GP28_QRT_STATE1	GPIO28	
GP32	(GPIO32)	
GP33	GPIO33(page26)	
GP17_TACH0	ISOLATEB_1	
GP7_TACH3	ISOLATEB_2	
GPI049	預留pull high、down	



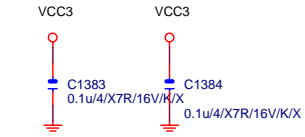
## ICH9

## DMI

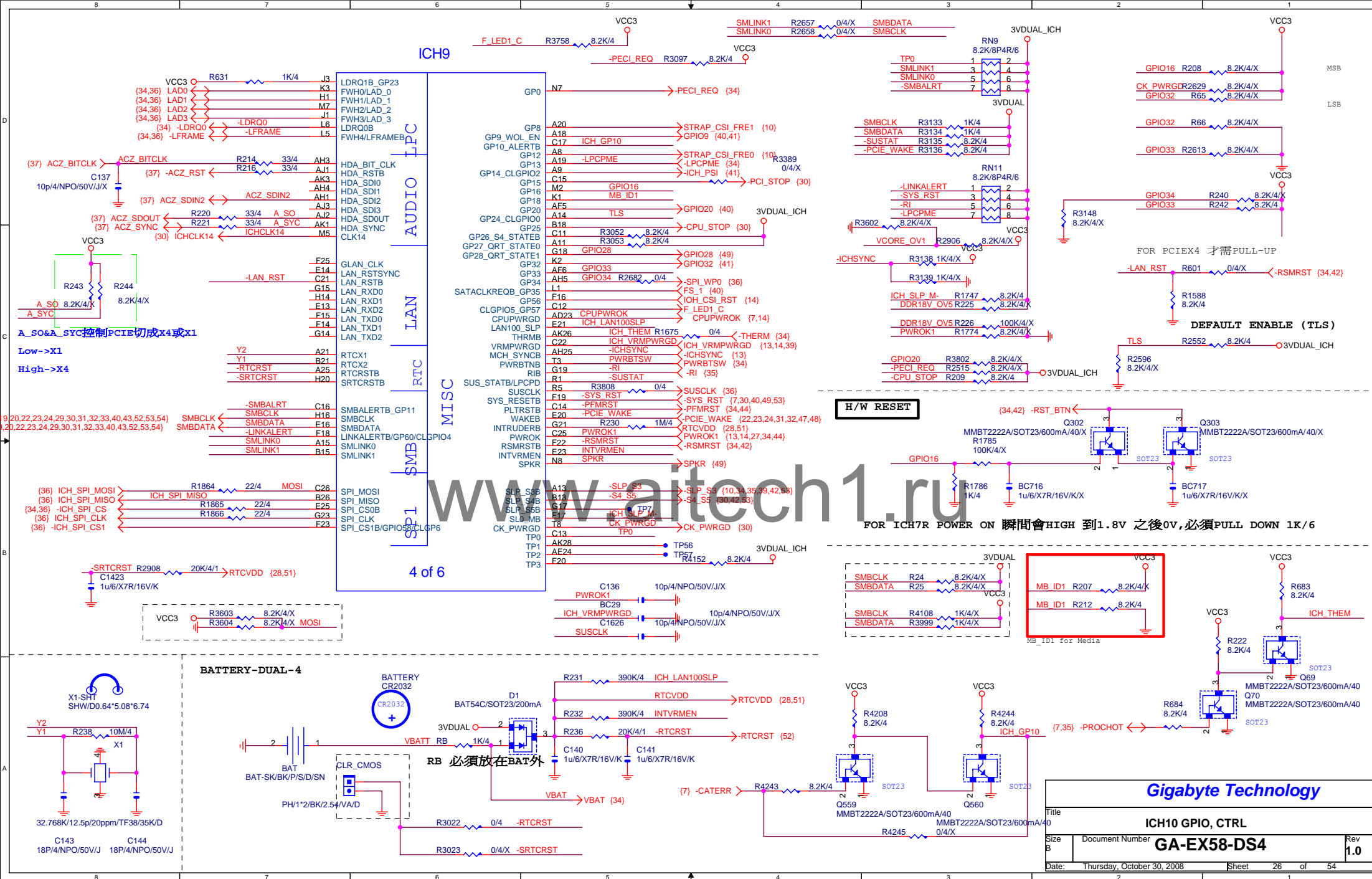
## USB

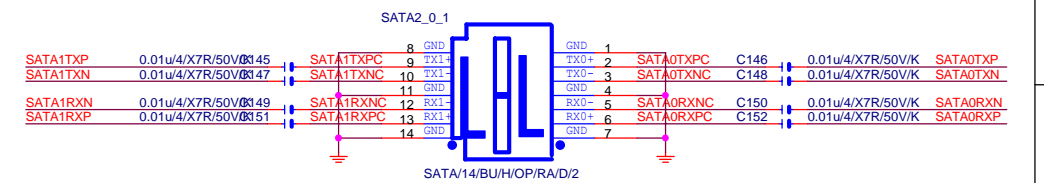
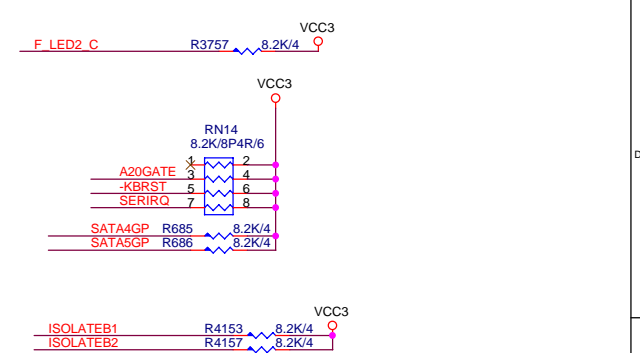
## PCI-E

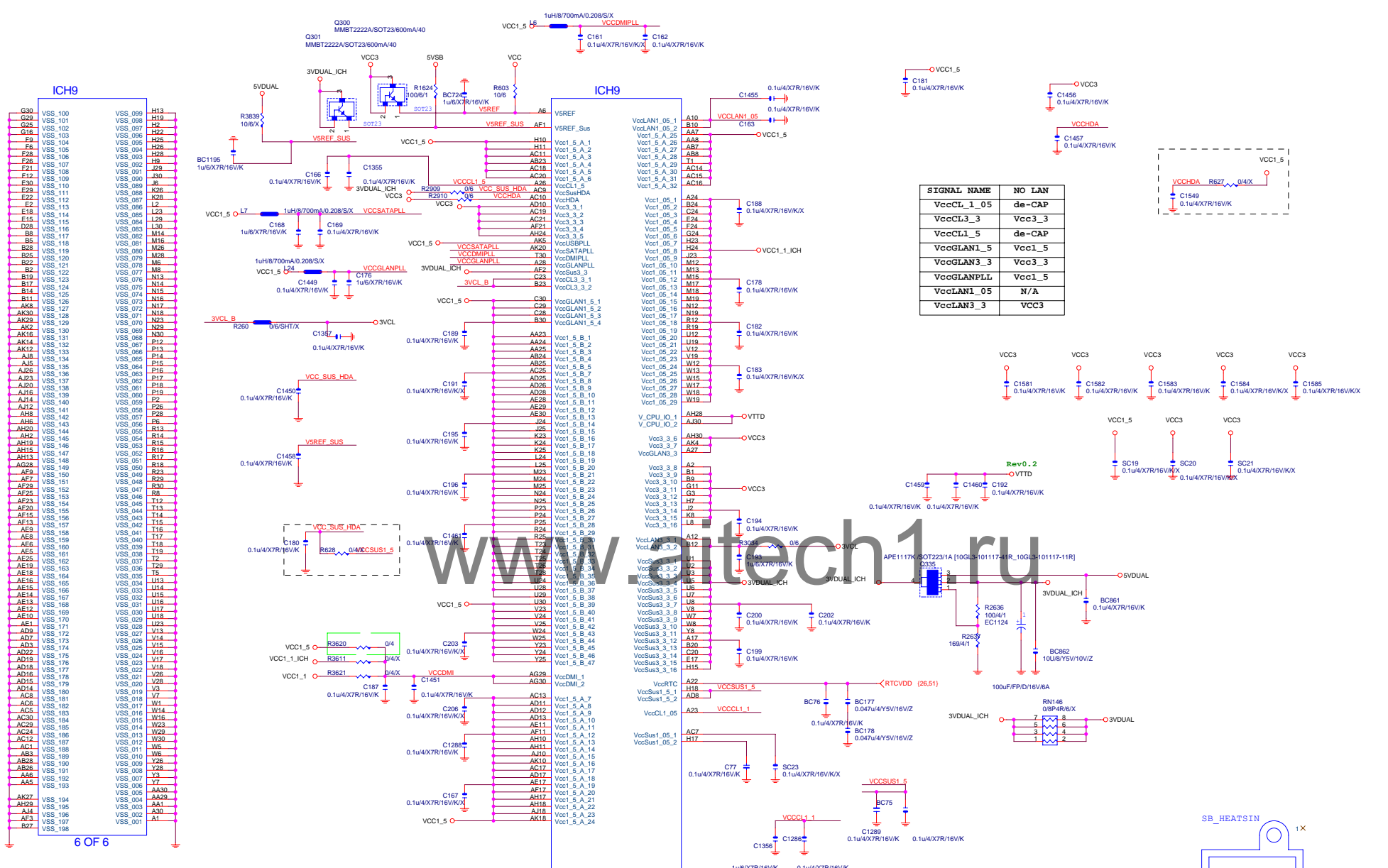
2 OF 6



Gigabyte Technology			
Title ICH10 DMI, PCI, USB			
Size B	Document Number	GA-EX58-DS4	
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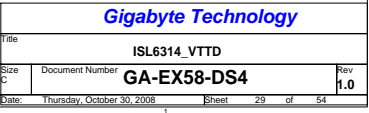




SIGNAL NAME	NO LAN
VccCL_1_05	de-CAP
VccCL_3_3	Vcc_3_3
VccCL_1_5	de-CAP
VccGLAN_1_5	Vcc_1_5
VccGLAN_3_3	Vcc_3_3
VccGLANPLL	Vcc_1_5
VccLAN_1_05	N/A
VccLAN_3_3	VCC3

SB\_HEATSIN[12SP2-030030-91R\_12SP2-030030-92R\_12SP2-030030-93R]

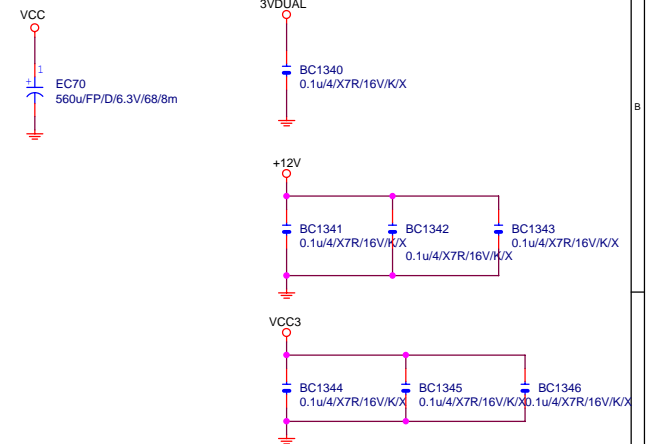
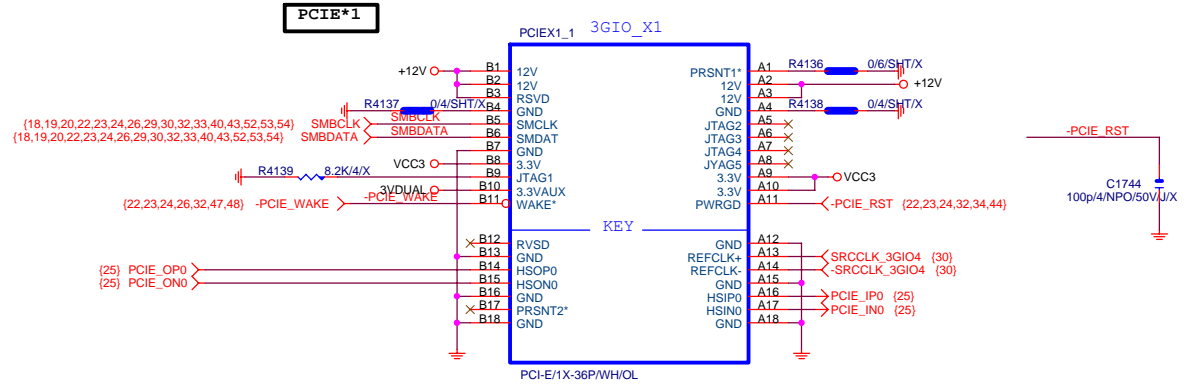






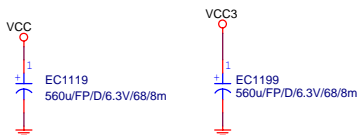


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Gigabyte Technology			
Title			
PCI EXPRESS X1			
Size	Document Number	Rev	
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EXP D TXP3 C1694 0.1u/4/X7R/16V/K EXP D TXP3C  
 EXP D TXN3 C1695 0.1u/4/X7R/16V/K EXP D TXN3C  
 EXP D TXP2 C1696 0.1u/4/X7R/16V/K EXP D TXP2C  
 EXP D TXN2 C1697 0.1u/4/X7R/16V/K EXP D TXN2C  
 EXP D TXP1 C1698 0.1u/4/X7R/16V/K EXP D TXP1C  
 EXP D TXN1 C1699 0.1u/4/X7R/16V/K EXP D TXN1C  
 EXP D TXP0 C1700 0.1u/4/X7R/16V/K EXP D TXP0C  
 EXP D TXN0 C1701 0.1u/4/X7R/16V/K EXP D TXN0C



EXP D TXP[0..3] >>> EXP\_D\_TXP[0..3] (12)  
 EXP D TXN[0..3] >>> EXP\_D\_TXN[0..3] (12)  
 EXP D RXP[0..3] >>> EXP\_D\_RXP[0..3] (12)  
 EXP D RXN[0..3] >>> EXP\_D\_RXN[0..3] (12)

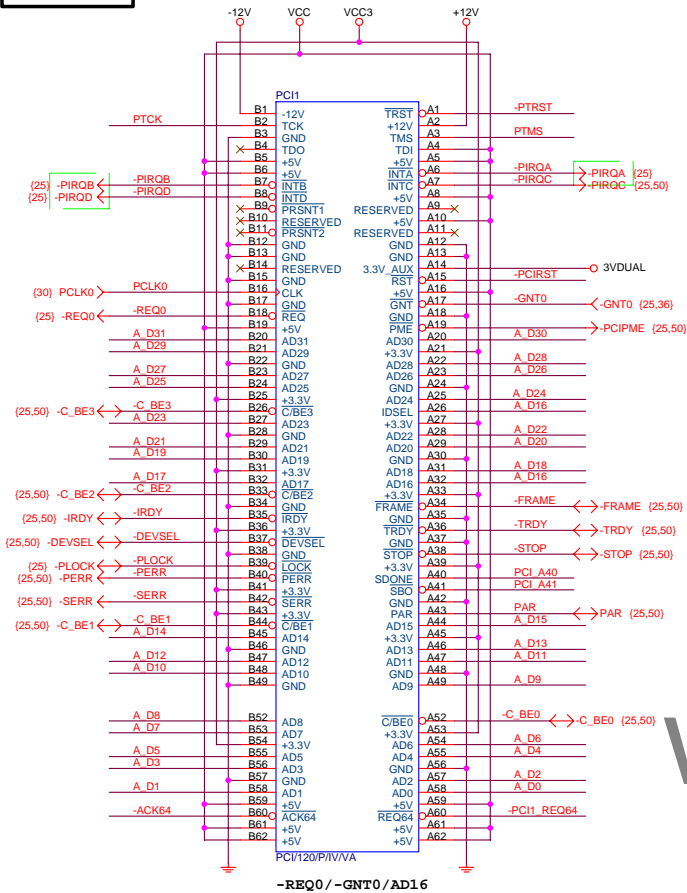
Color must  
be Orange

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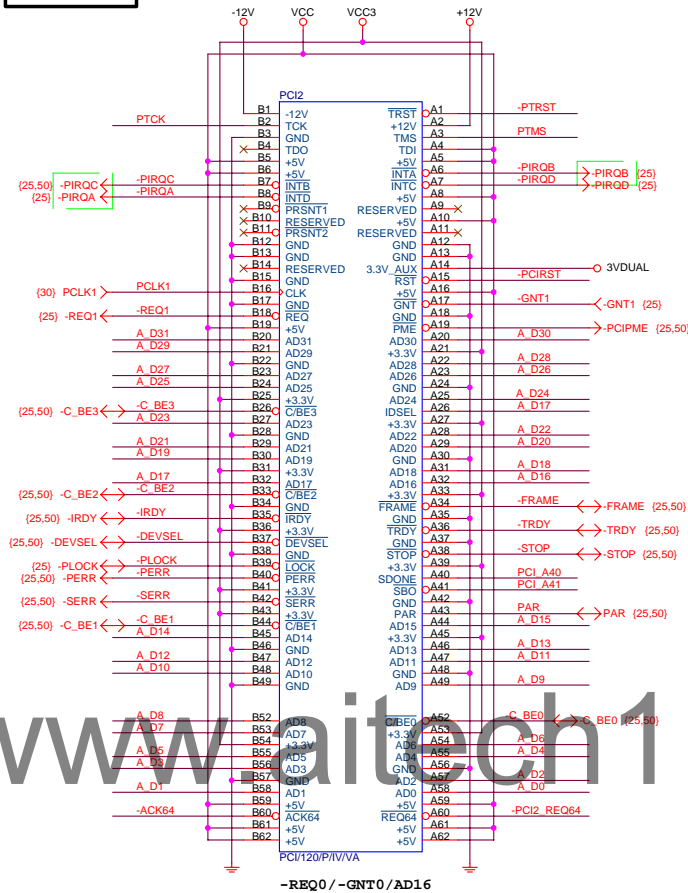
PCI-E/4X-64P/OR/OL/OPEN/[11AC1-021064-31R\_11AC1-021064-32R]

Gigabyte Technology		
Title		
PCI EXPRESS X 4 PORT		
Size	Document Number	Rev
Custom	GA-EX58-DS4	1.0
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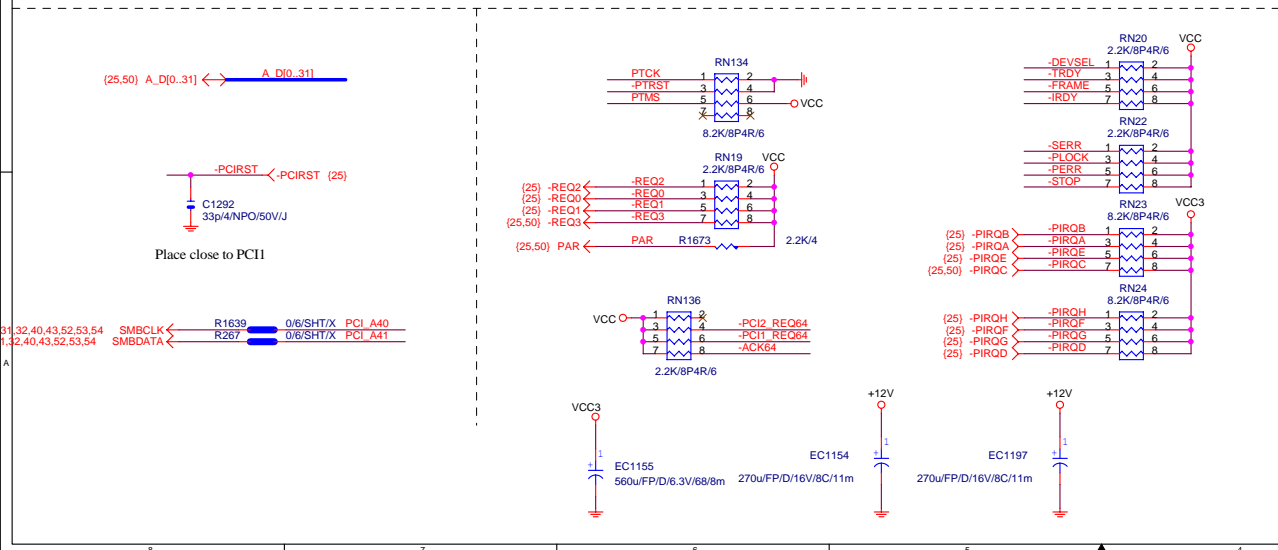
## PCI1,2 SLOT



## PCI1,2 SLOT

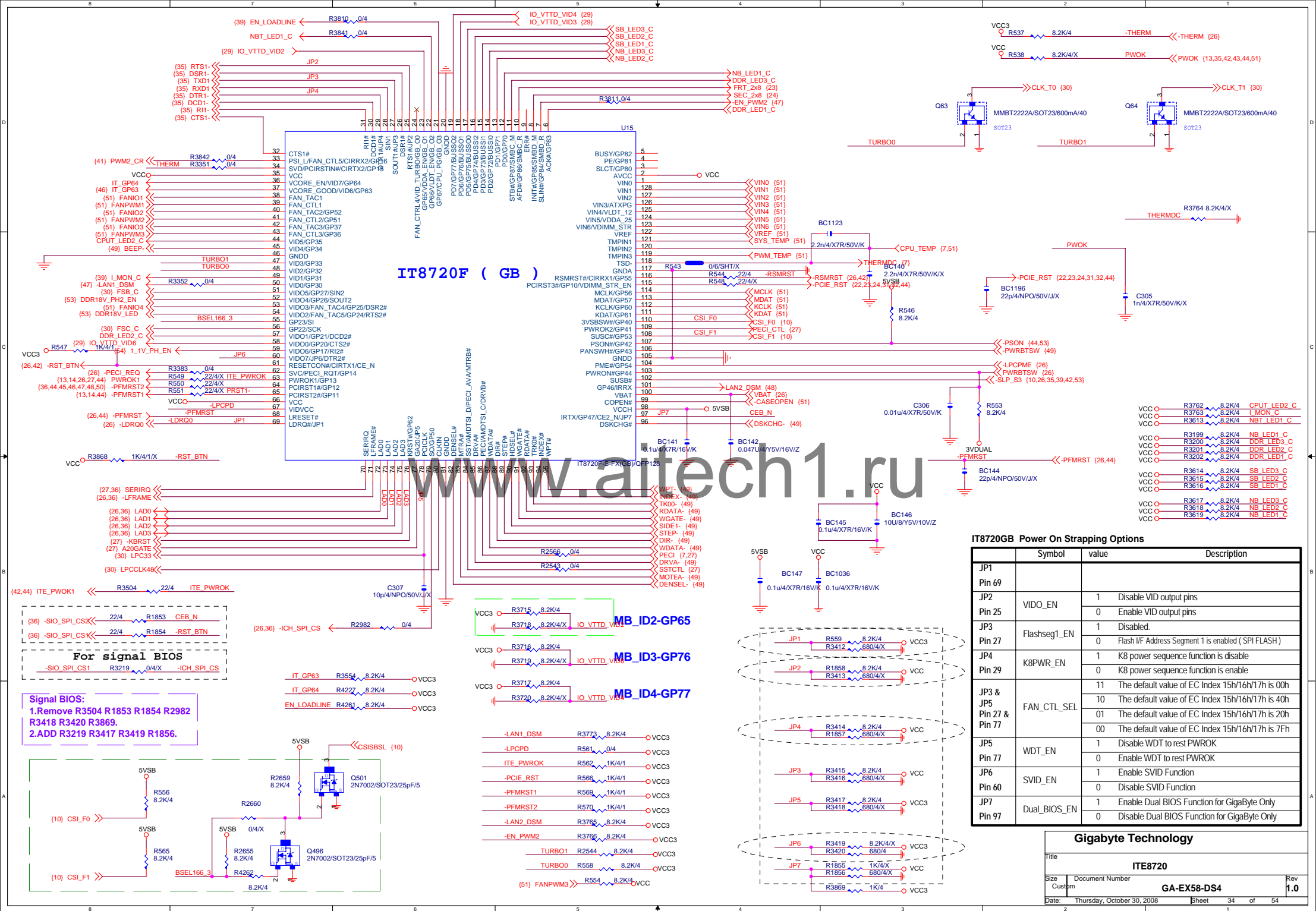


PIN NAME	USAGE	NOTE
FAN_TAC2/GP52	FANIO2	
FAN_TAC3/GP37	FANIO3	
VIDO3/FAN_TAC4/GP25/DSR2#	FANIO4	
FAN_CTL2/GP51	FANPWM2	
FAN_CTL3/GP36	FANPWM3	
VID4/GP34	BEEP-	
VID3/GP33	TURBO1	
VID2/GP32	TURBO0	
VCORE_GOOD/VID6/GP63	CPUT_LED1_C	
VID5/GP35	CPUT_LED2_C	
VID1/GP31	CPUT_LED3_C	
VID0/GP30	-LAN1_DSM	NBT_LED1_C
SLCT/GP80	CPU_LED1_C	
PE/GP81	CPU_LED2_C	
BUSY/GP82	CPU_LED3_C	
PD3/GP73/BUSS11	SB_LED1_C	
PD4/GP74/BUSS12	SB_LED2_C	
VCORE_EN/VID7/GP64	SB_LED3_C	
PD0/GP70	NB_LED1_C	
PD1/GP71	NB_LED2_C	
PD2/GP72/BUSS10	NB_LED3_C	
GP22/SCK	LOW_PWR_1	
VIDO5/GP27/SIN2	LOW_PWR_2	
PCIRST2#/GP11	-PFMRST1	
PCIRST1#/GP12	-PFMRST2	
3VBSBW#/GP40	CSI_F0	BSEL166_1
SUSC#/GP53	CSI_F1	BSEL166_2
GP23/SI	BSEL166_3/CSISBSL	
VIDO0/GP20/CTS2#	BSEL166_4	
GP65/VDDA_EN/GB_01	MB_ID2	
PD6/GP76/BUSS01	MB_ID3	
PD7/GP77/BUSS02	MB_ID4	
AFD#/GP86/SMBC_R	FST_2X8	GTLREF_AD1
INIT#/GP85/SMBC_M	SEC_2x8	GTLREF_AD2
ACK#/GP83	DDR_LED1_C	
VIDO1/GP21/DCD2#	DDR_LED2_C	
STB#/GP87/SMBC_M	DDR_LED3_C	
PWRON#GP44	VCORE_OV1	
PANSWH#/GP43	PWRBTSW	
KDAT/GP61	-PWRBTSW	
KCLK/GP60	KDAT	
MDAT/GP57	KCLK	
MACL/GP56	MDAT	
GP66/VLDT_EN/GB_02	NBT_LED1_C	MCLK
SVD/PCIRSTIN#/CIRTX/GP15	PWM2_CR	
KDAT/GP61	PWM2_CR	
GP67/CPU_PG/GB_03	IT_GP67/-EN_PWM2	
SLIN#/GP84/SMBC_R	-EN_PWM2	
PSI_L/FAN_CLT5/CIRRX2/GP16	-THERM	
VIDO4/GP26/SOUT2	DDR18V_PH2_EN	
VIDO2/FAN_TAC5/GP24/DSR2#	DDR18V_LED	
VIDO6/GP17/RI2#	1_1V_PH_EN	
VIDO7/JP6/DTR2#	JP6	

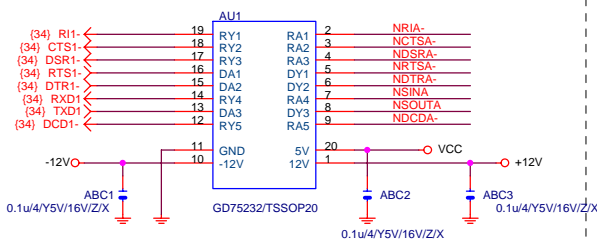


Gigabyte Technology

Title			PCI SLOT 1, GA-EX58-DS4
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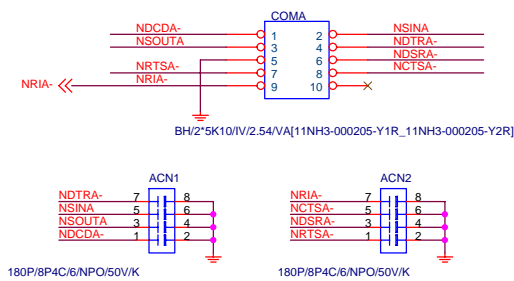
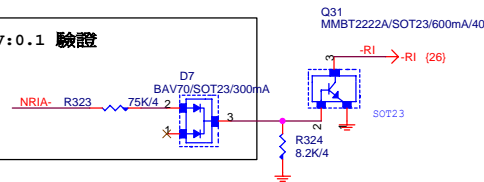


COMA

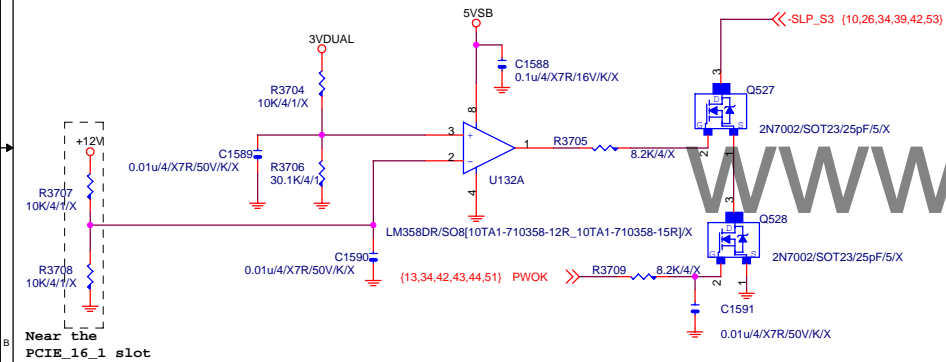


COM RI

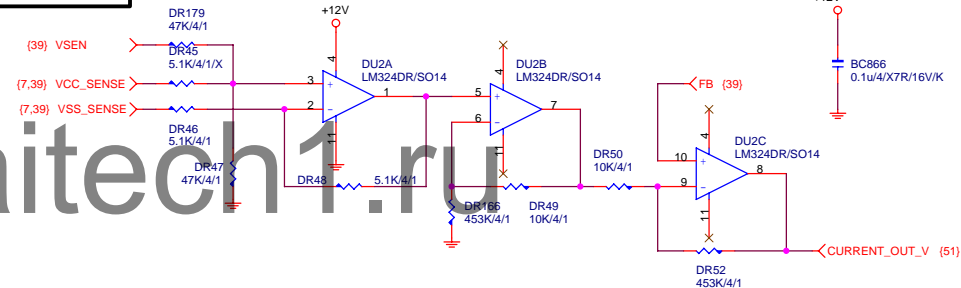
REV:0.1 驗證



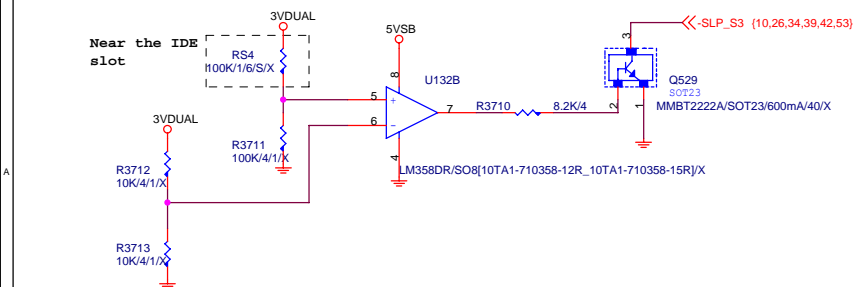
+12V保護線路-1



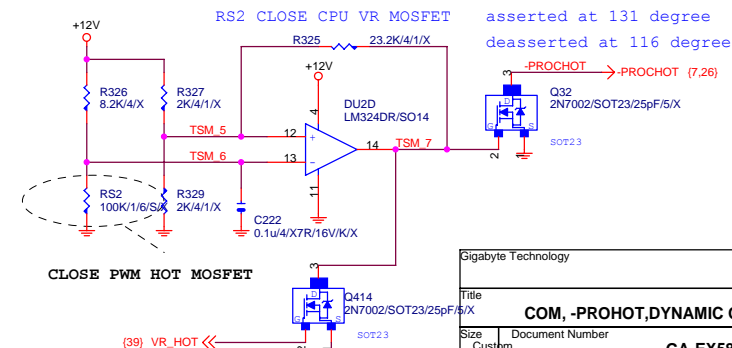
DYNAMIC CURRENT OC



+12V保護線路-2



-PROHOT

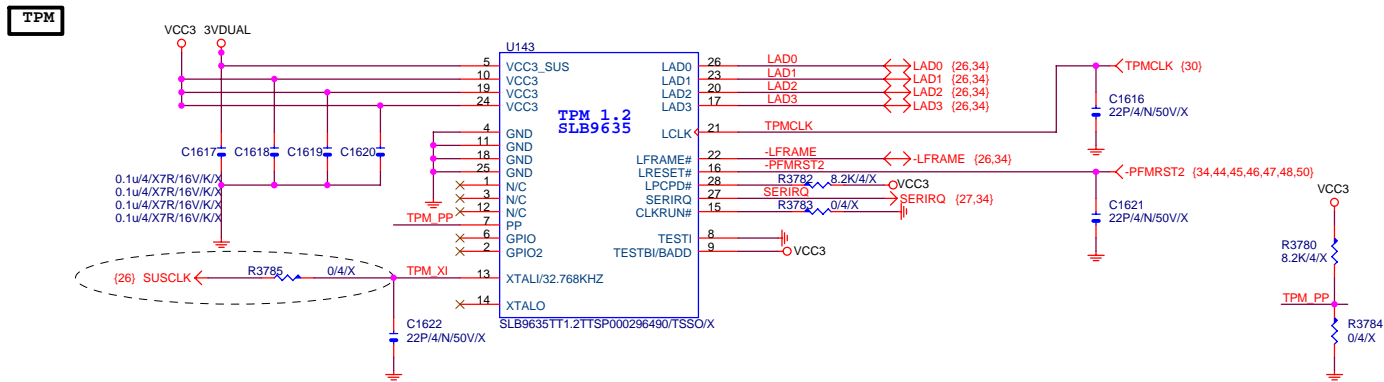
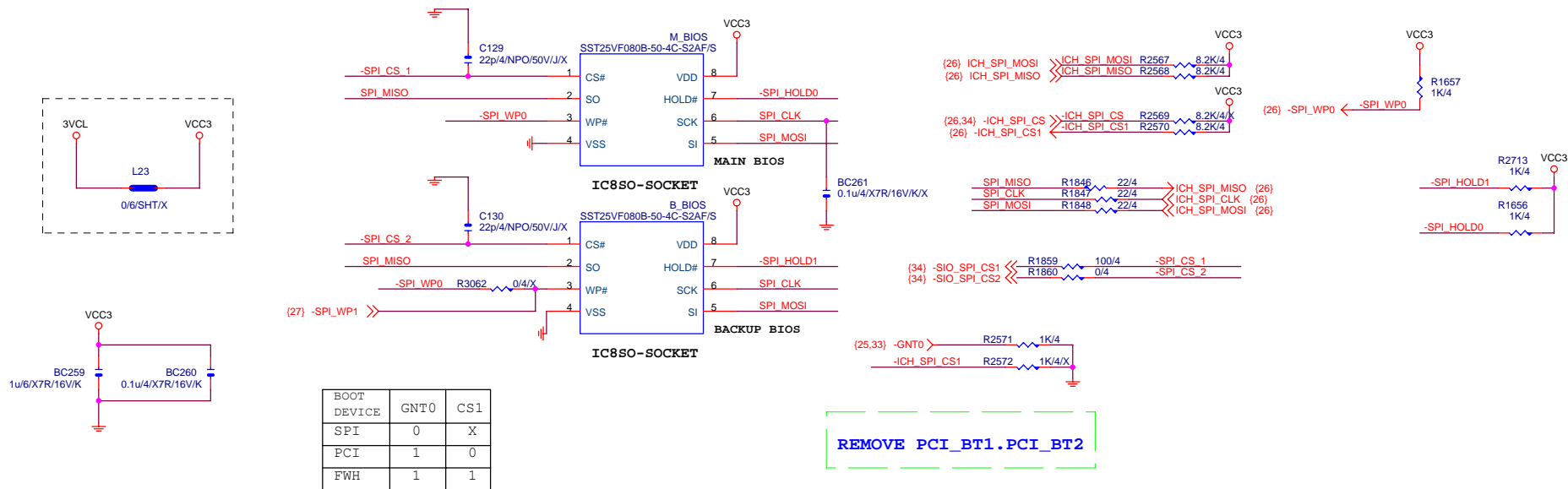


Gigabyte Technology

Title  
COM, -PROHOT,DYNAMIC OC +12V保護線路Size  
Custom

Date: Thursday, October 30, 2008

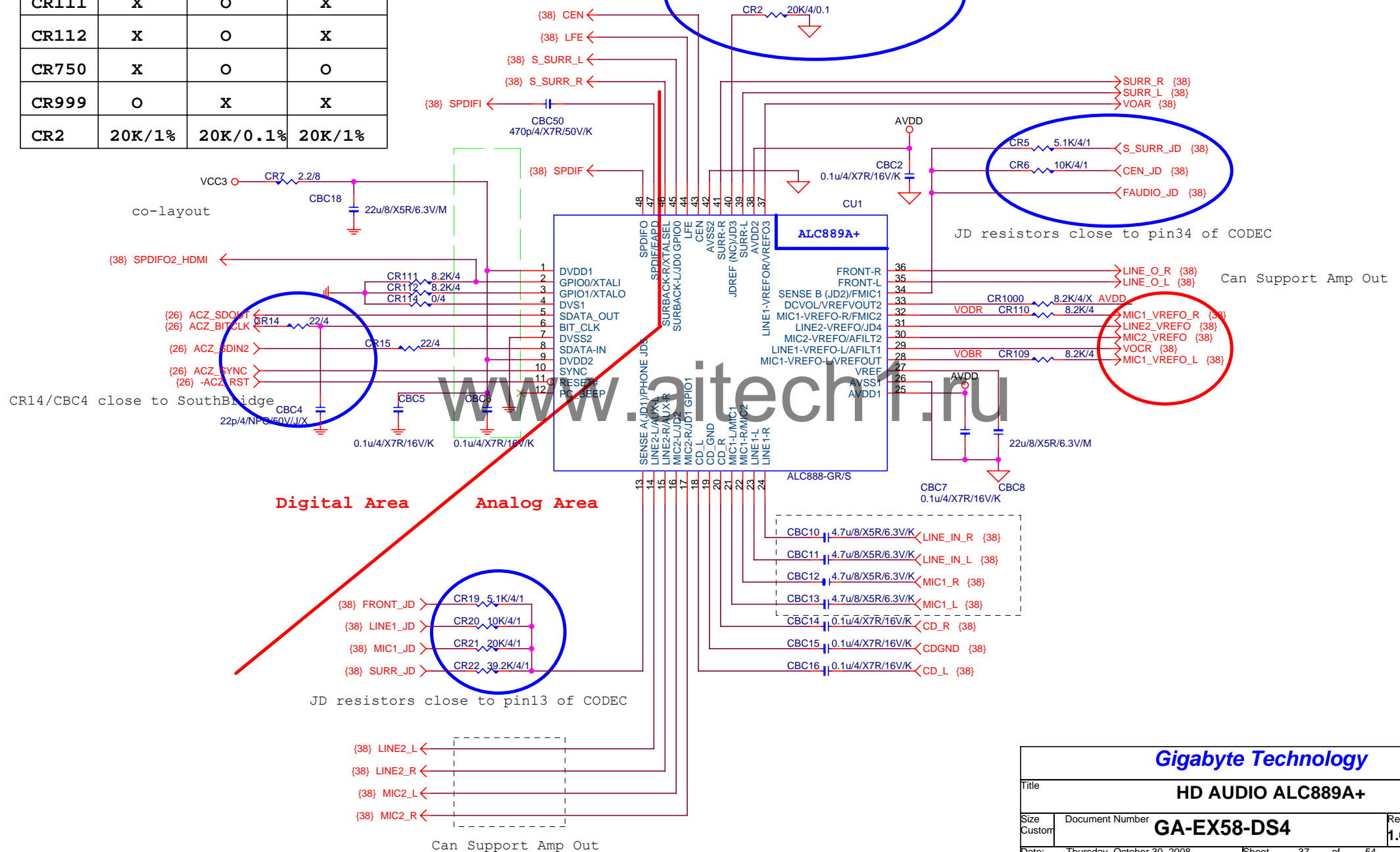
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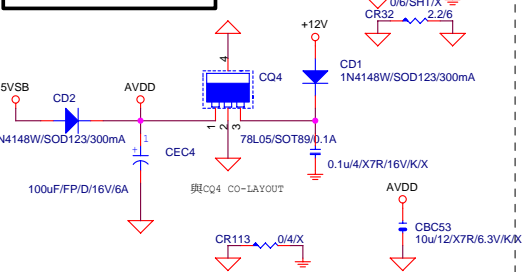
## DUAL BIOS TPM



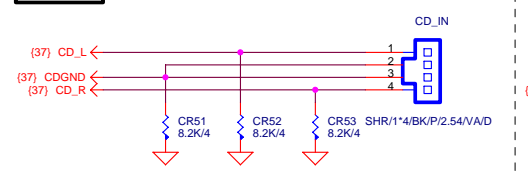
	ALC889A+	ALC889A	ALC888Vx
CR111	X	O	X
CR112	X	O	X
CR750	X	O	O
CR999	O	X	X
CR2	20K/1%	20K/0.1%	20K/1%



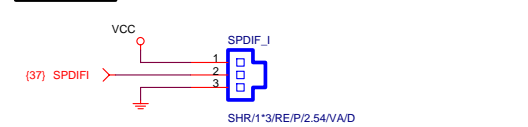
# CODEC POWER/EMI PAD



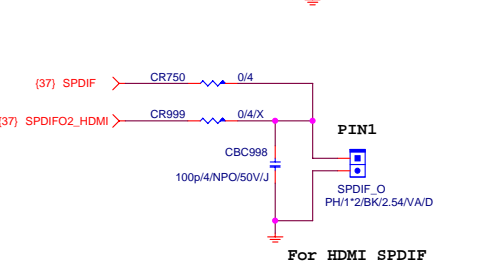
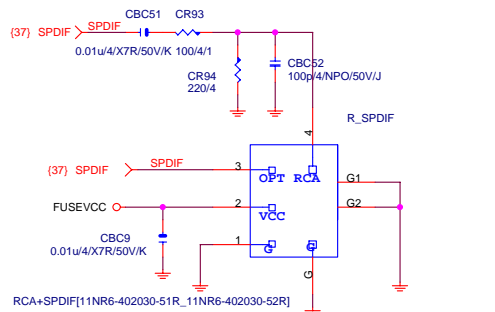
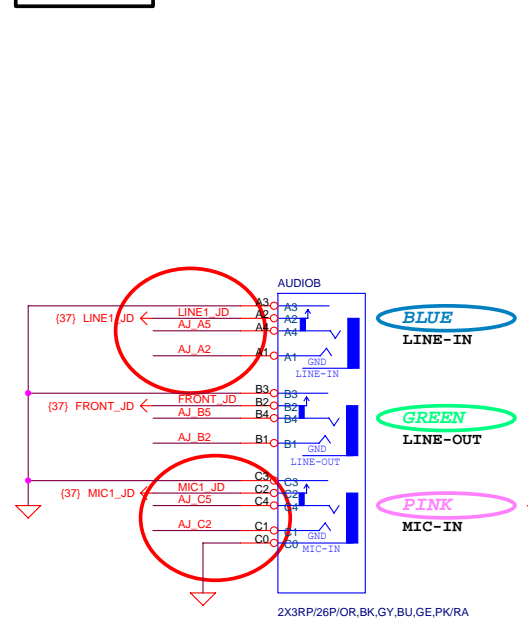
# CD IN



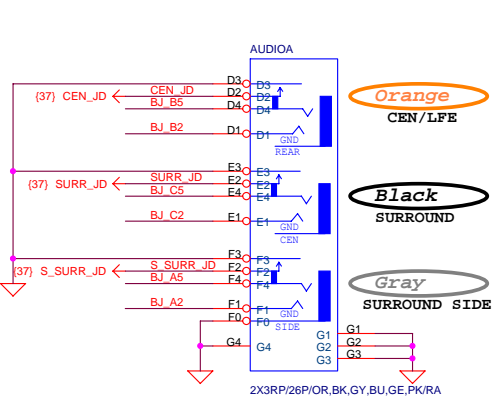
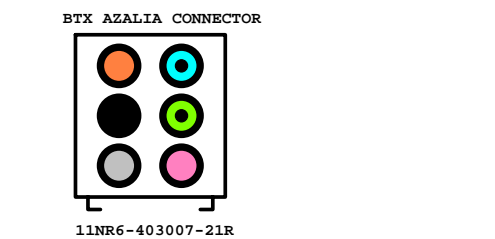
# SPDIF\_IN



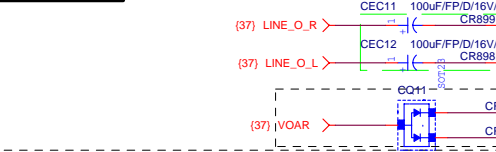
# AZALIA JACK



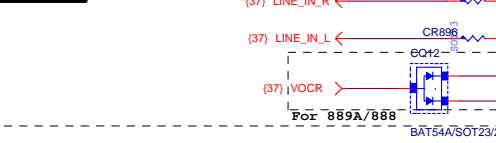
# EQ



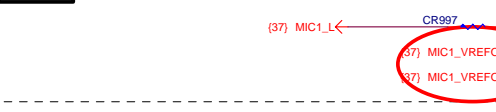
# LINE-OUT



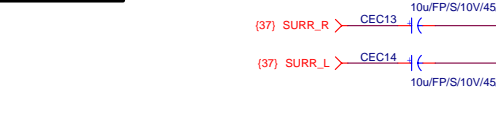
# LINE-IN



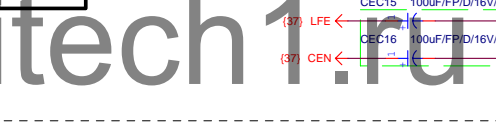
# MIC-IN



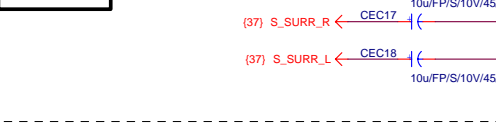
# SURROUND



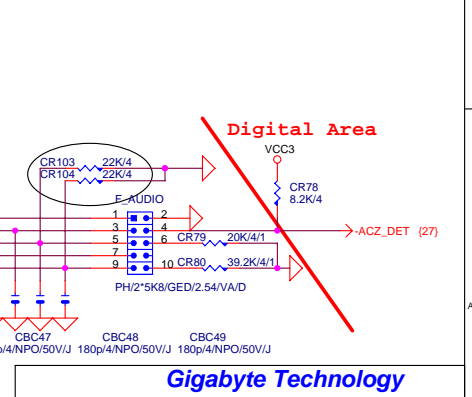
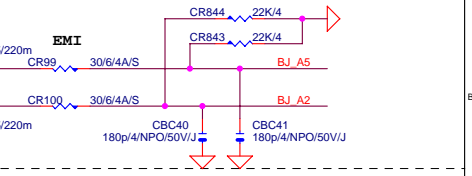
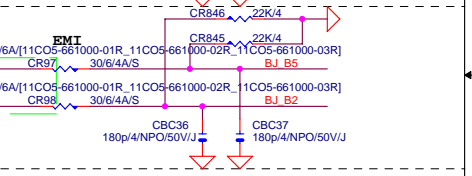
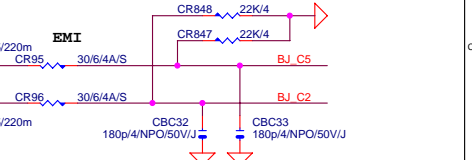
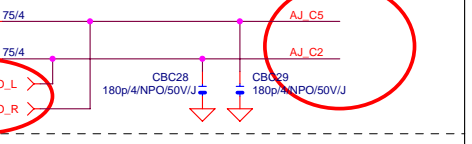
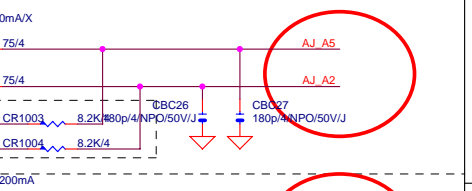
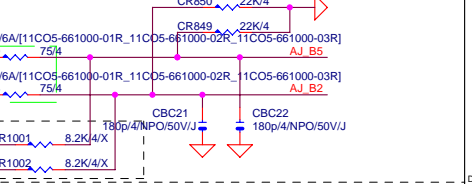
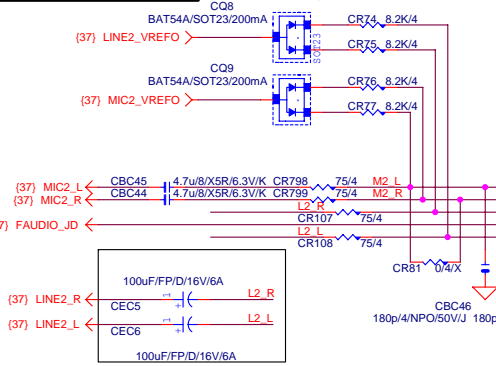
# CEN/LFE



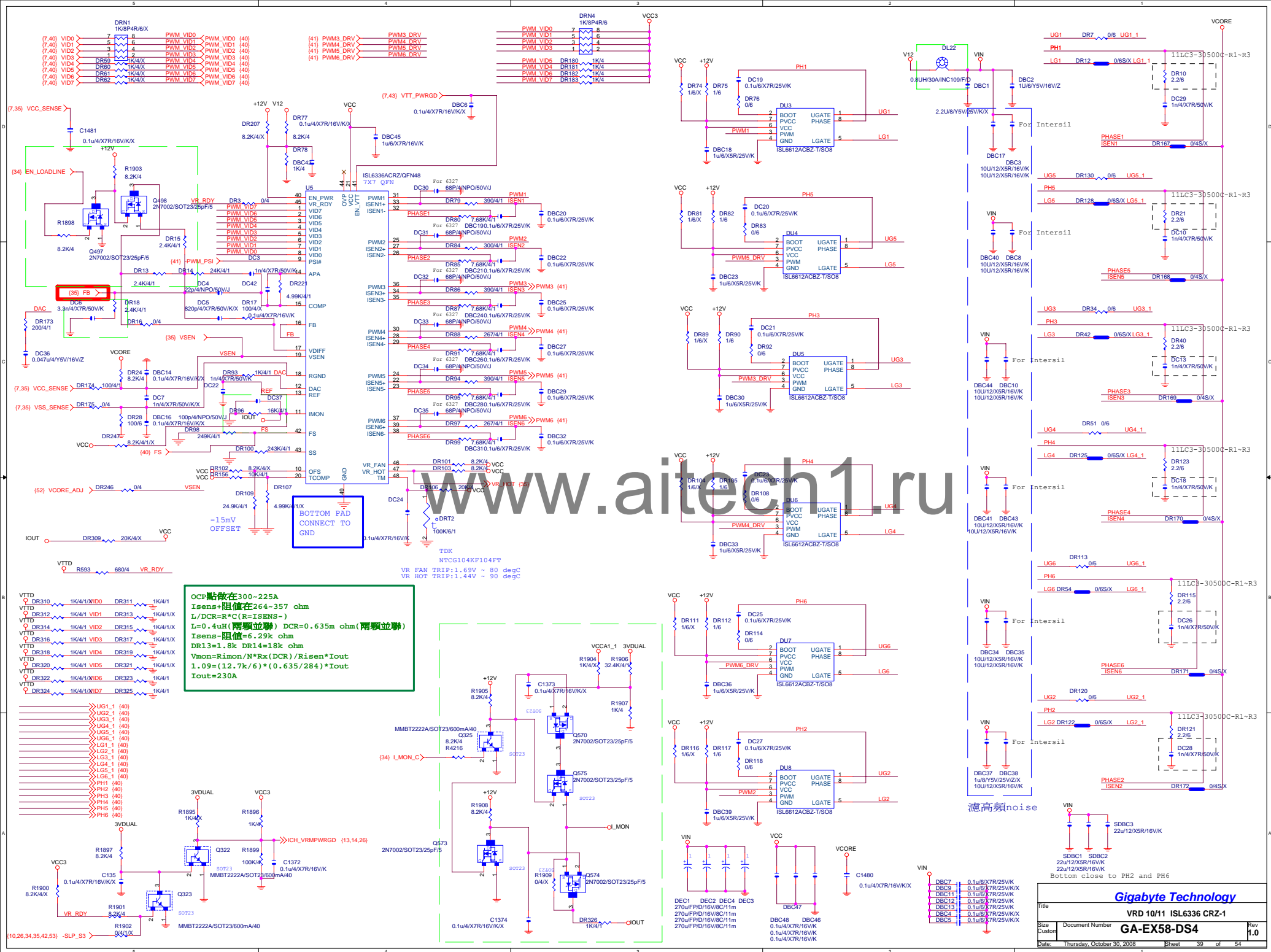
# SURR BACK



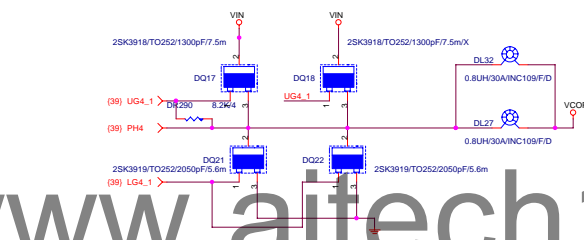
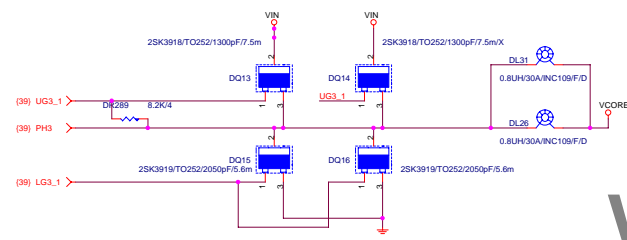
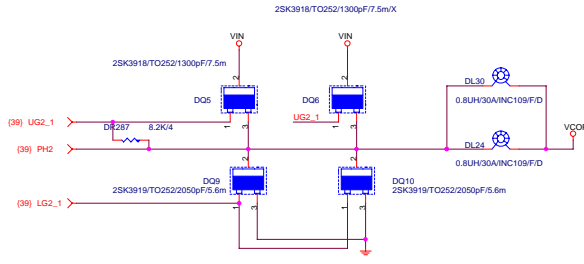
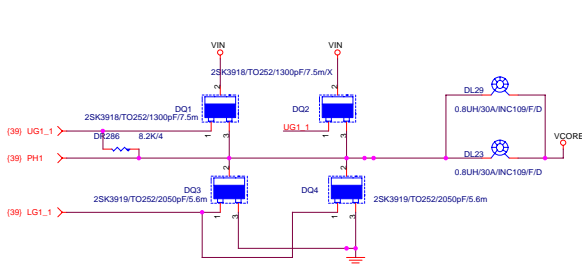
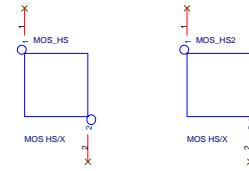
# AZALIA FRONT PANEL



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Title			
AUDIO JACK			
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# MOS HEATSINK

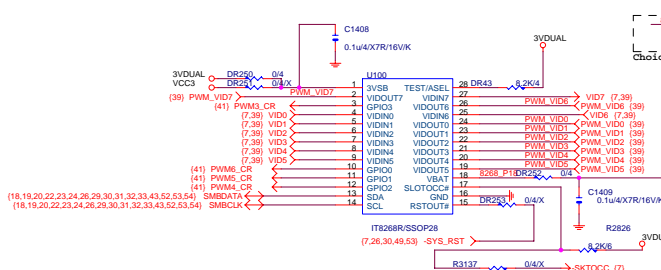
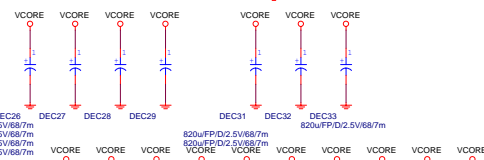
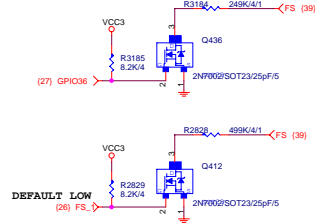
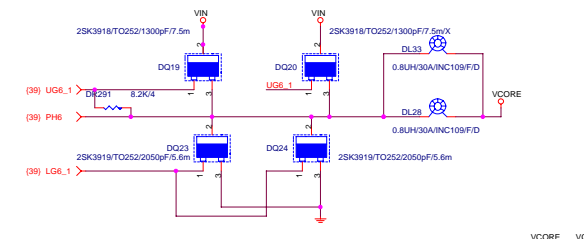
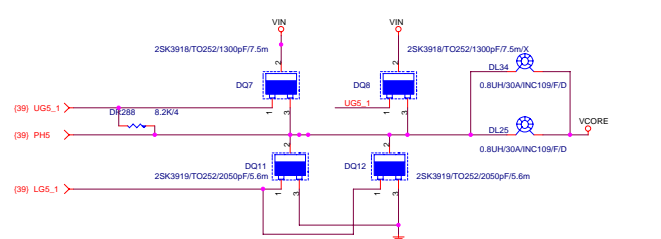


Switching Frequency:

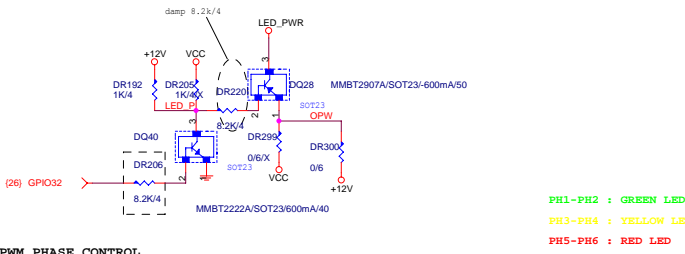
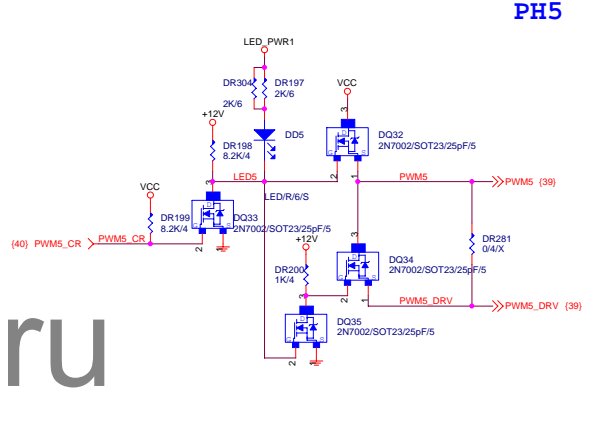
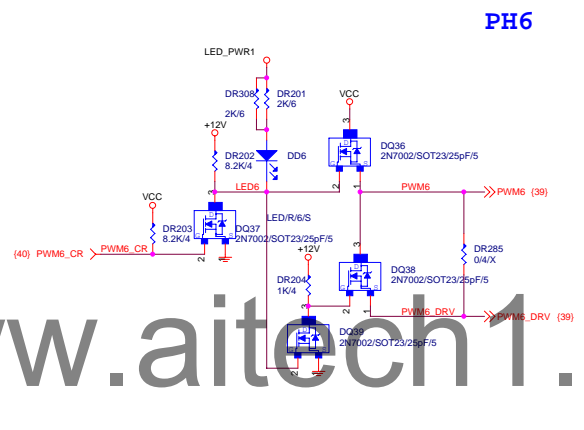
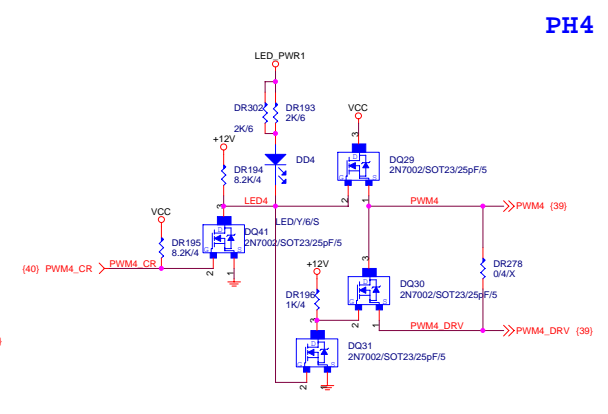
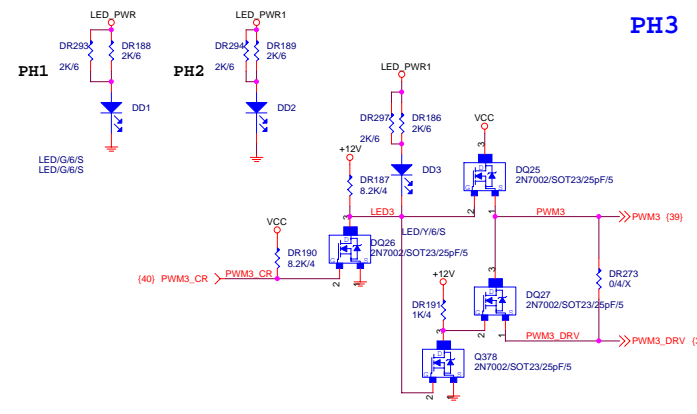
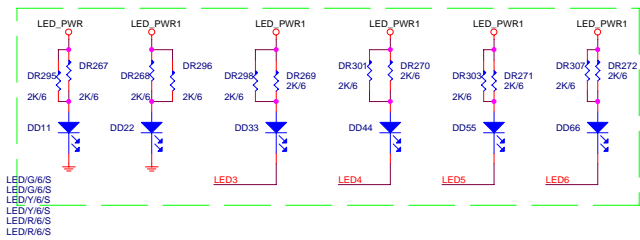
GPIO36	FS_1	Frequency
LO	LO	100KHz
LO	HI	150KHz
HI	LO	200KHz
HI	HI	250KHz

Default

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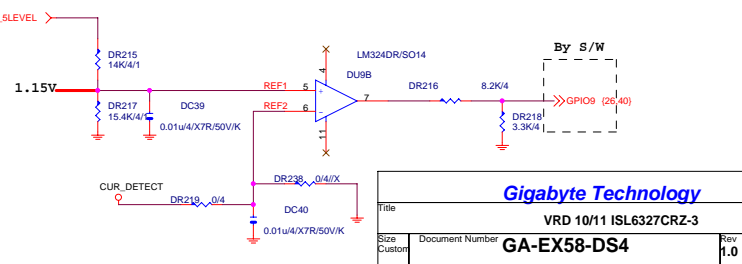
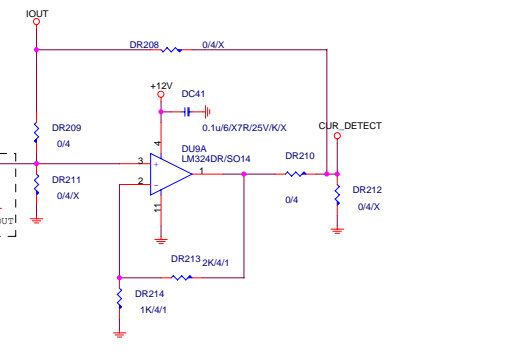
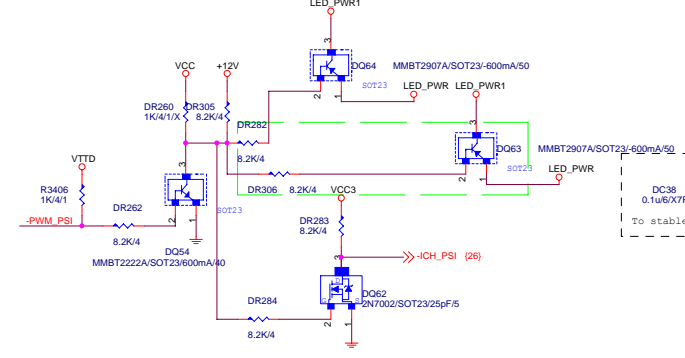
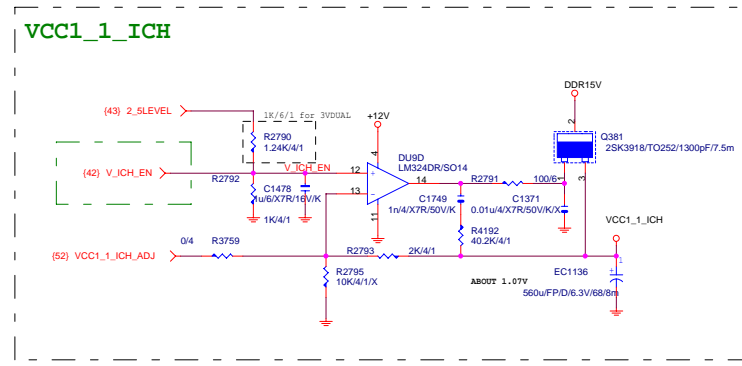
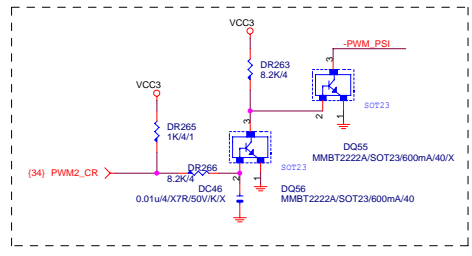
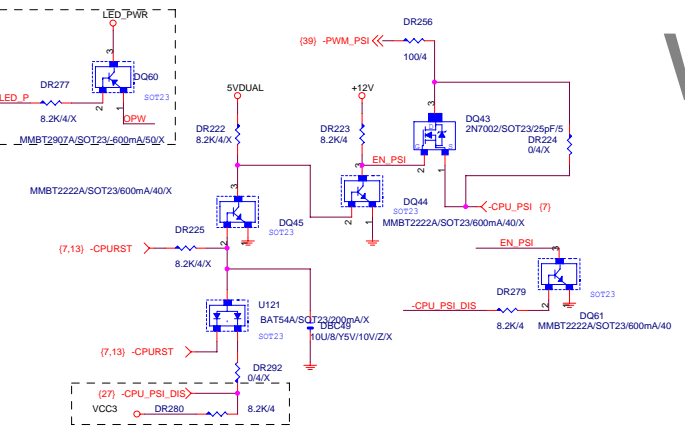
ITE 8268 +	ITE 8268
Pop	Pop
R3347	R3348
R3349	R3137
R3350	c1409



PWM PHASE CONTROL

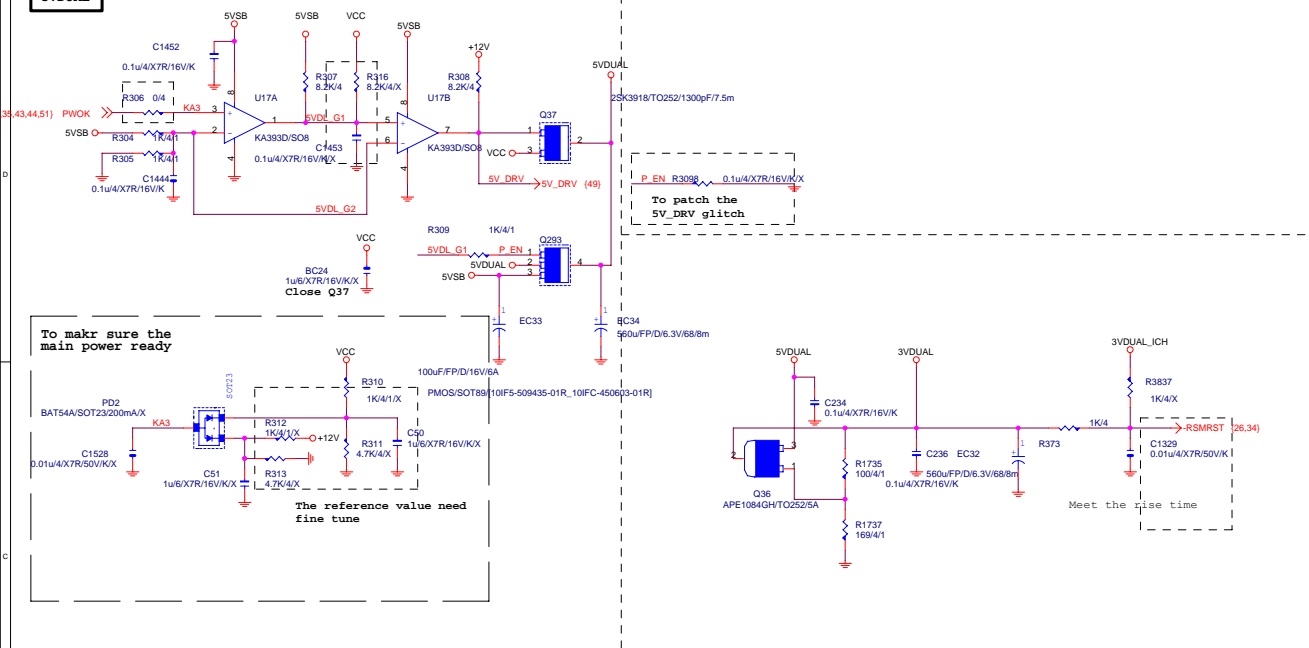
	ITE8268_GPIO3	ITE8268_GPIO2	ITE8268_GPIO1	ITE8268_GPIO0
2 PHASE	L	L	L	L
3 PHASE	X	L	L	L
4 PHASE	X	X	L	L
5 PHASE	X	X	X	L
6 PHASE	X	X	X	X

PH1-PH2 : GREEN LED  
PH3-PH4 : YELLOW LED  
PH5-PH6 : RED LED

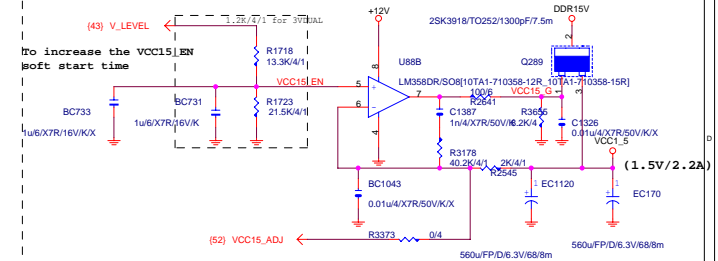


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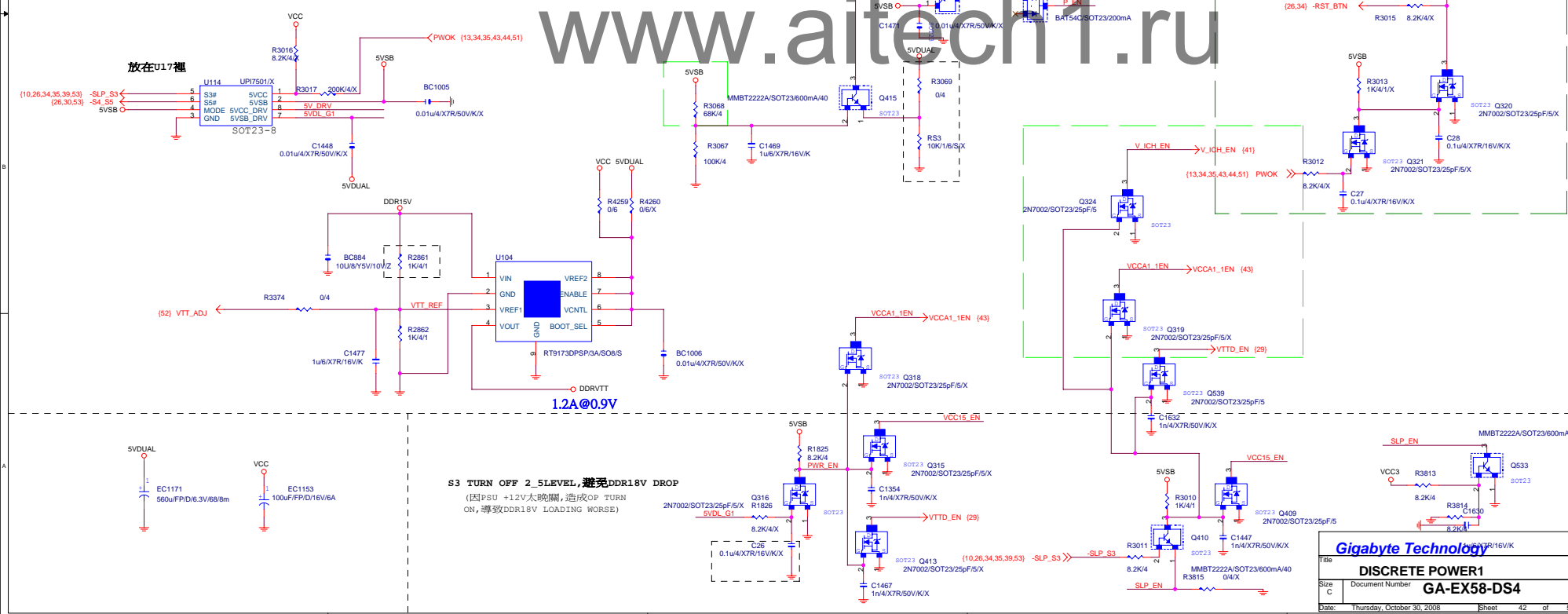
## 5VDUAL



VCC1\_5



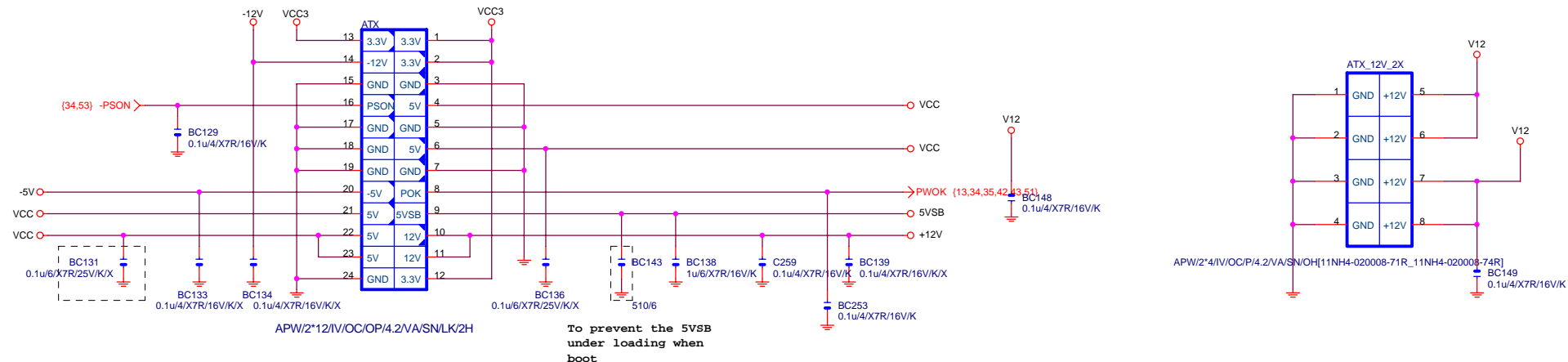
DDR18V/DDRVTT/VCC1_05/VTT_GMCH
--------------------------------



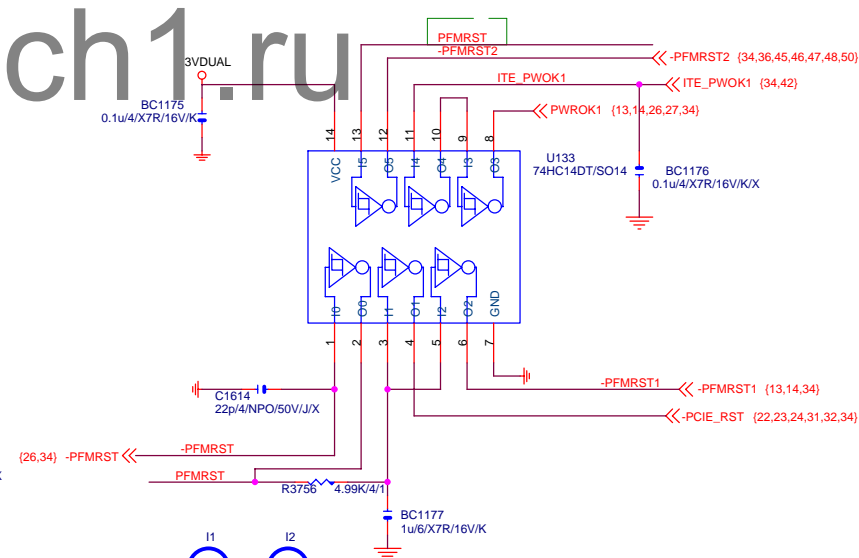
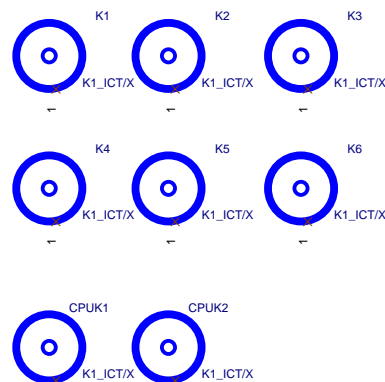
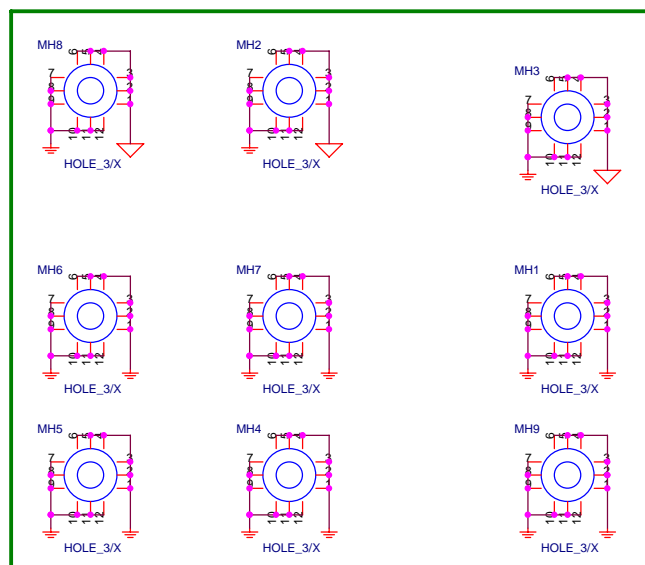




# ATX POWER CONNECTOR



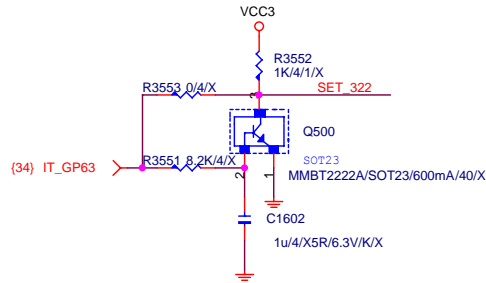
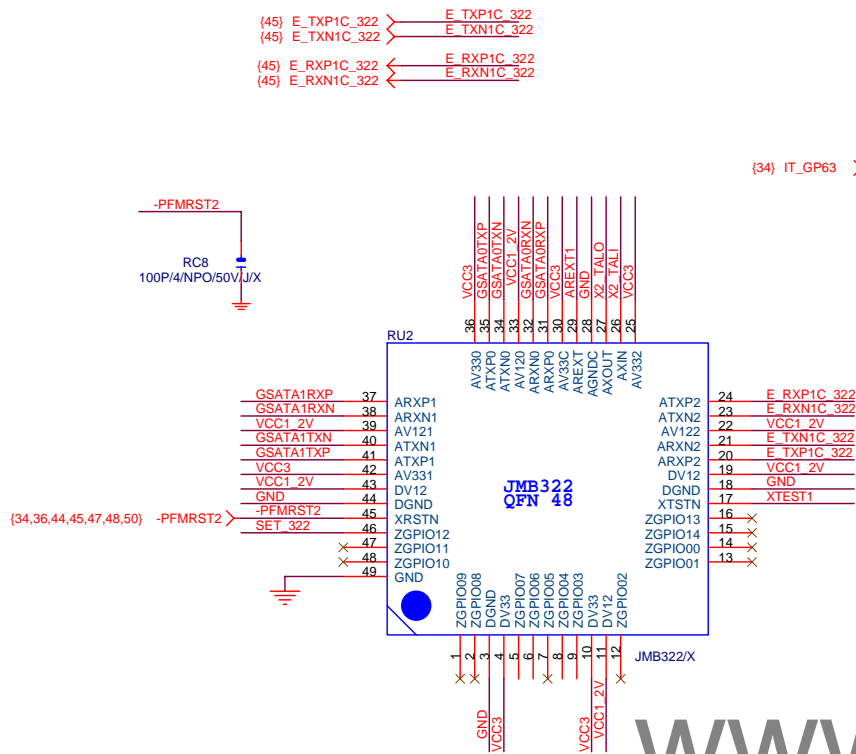
## PCB 螺絲孔位置(Footprint不同)



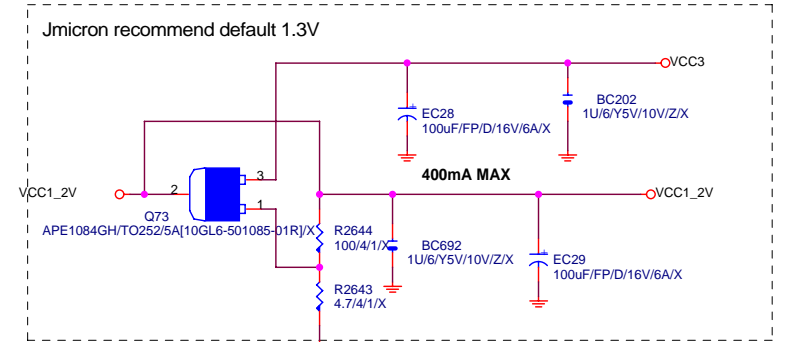
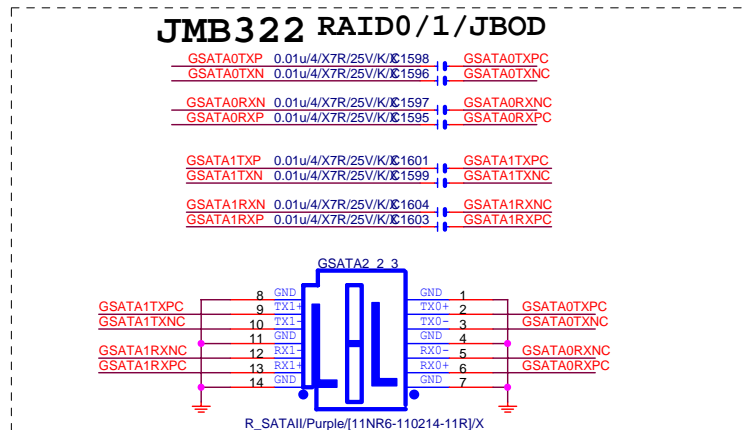
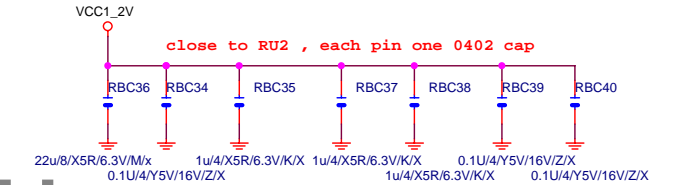
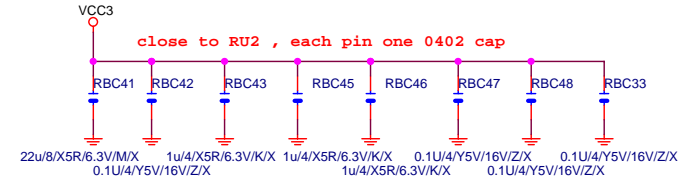
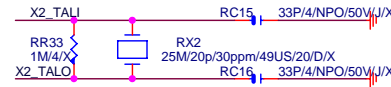
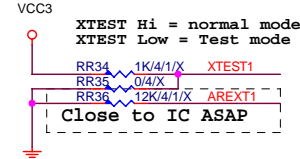
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Title		
ATX POWER CONNECTOR		
Size	Document Number	Rev
B	GA-EX58-DS4	1.0
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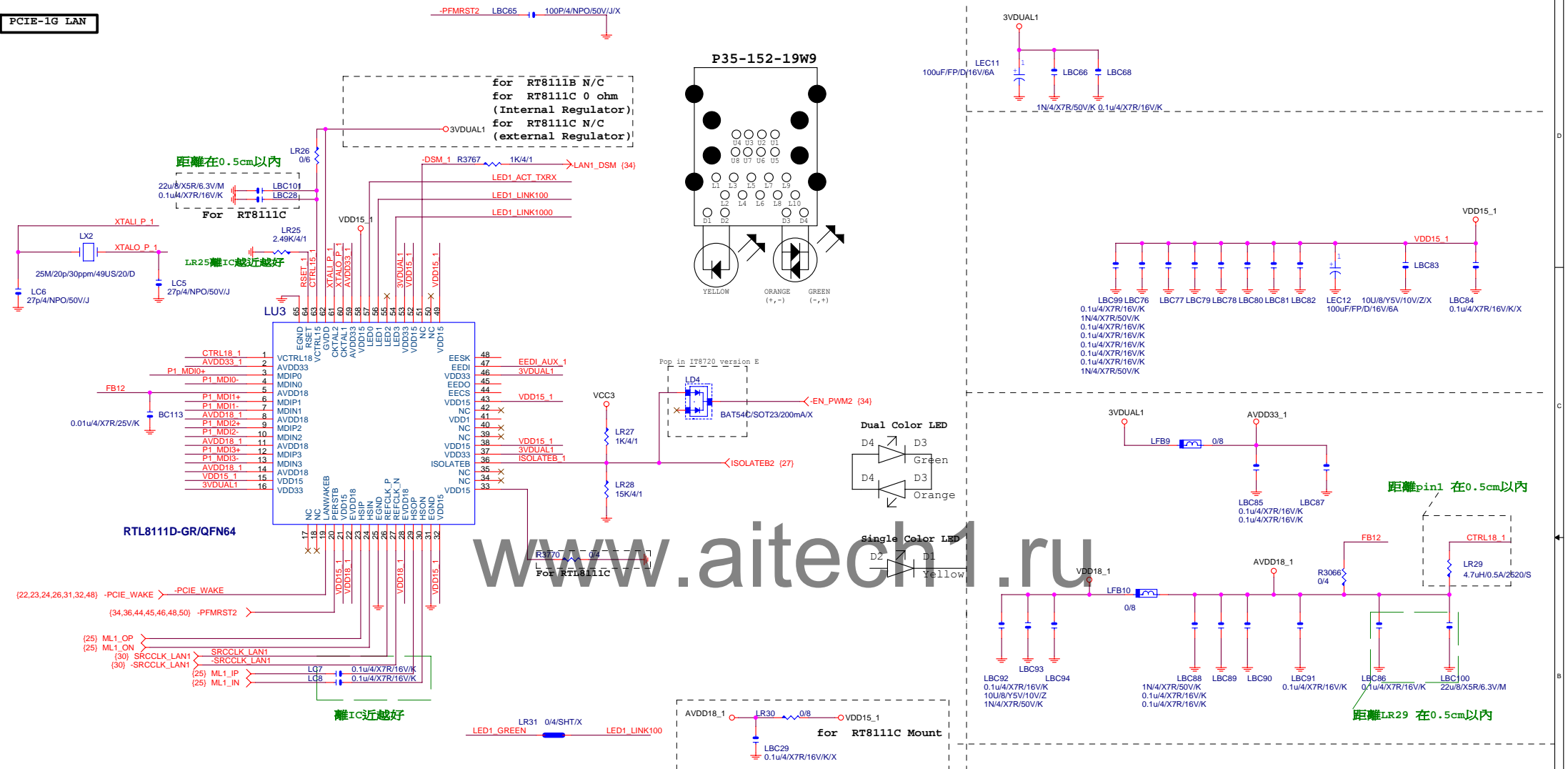
SET_322	
Low	Normal
To set JMB322	



Gigabyte Technology			
Title		JMB322	
Size	Document Number	GA-EX58-DS4	
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		Rev	1.0

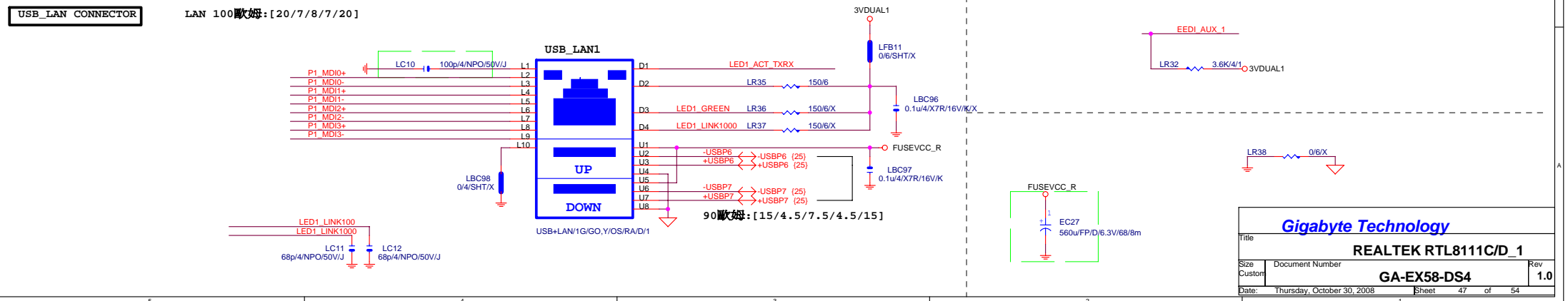
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## PCIE-1G LAN



## USB\_LAN CONNECTOR

LAN 100 歌姆:[20/7/8/7/20]



## Gigabyte Technology

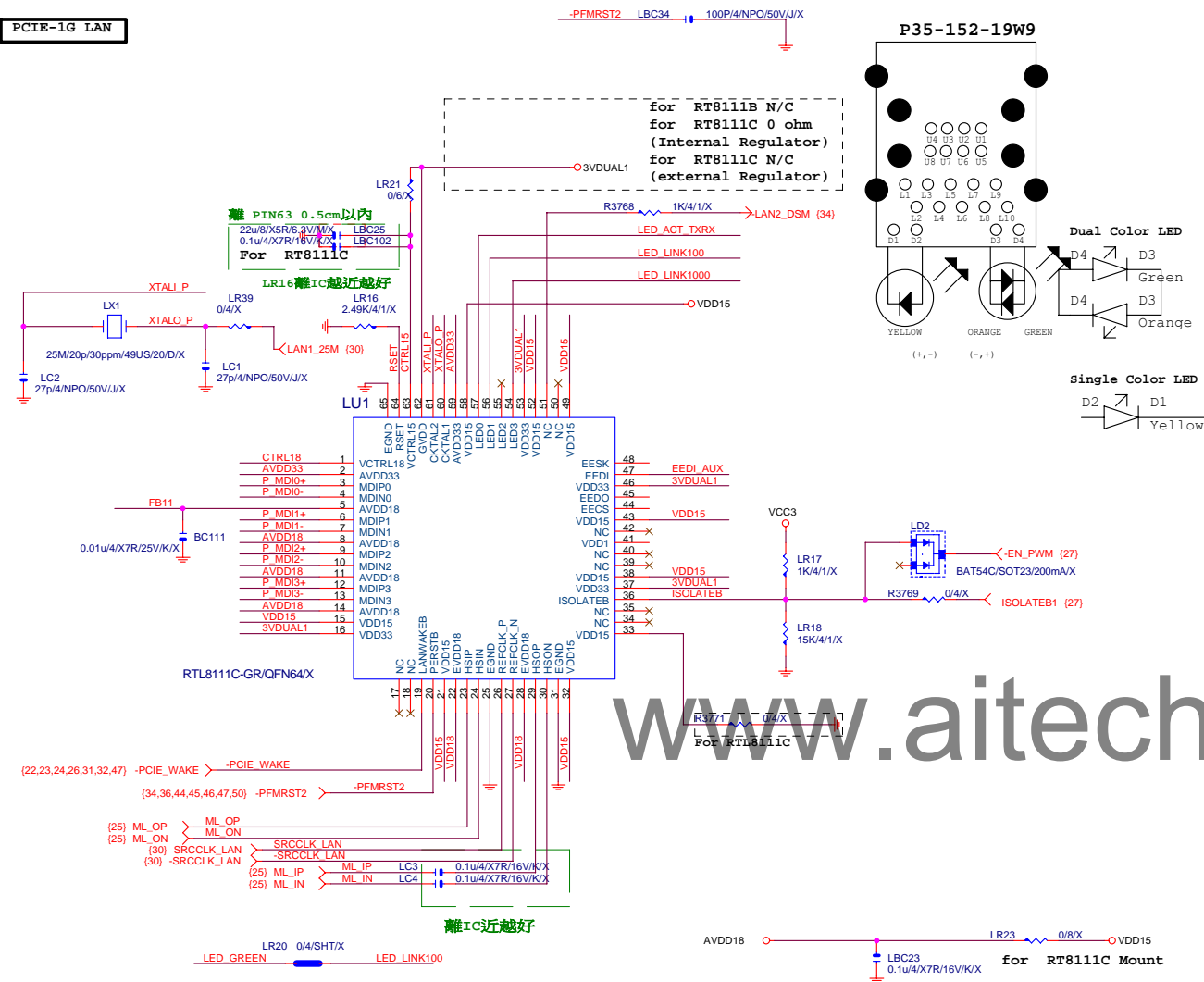
Title **REAL TEK PTL 8111C/D 1**

Size	Document Number	Rev
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Custom	<b>GA-EX58-DS4</b>	<b>1.0</b>
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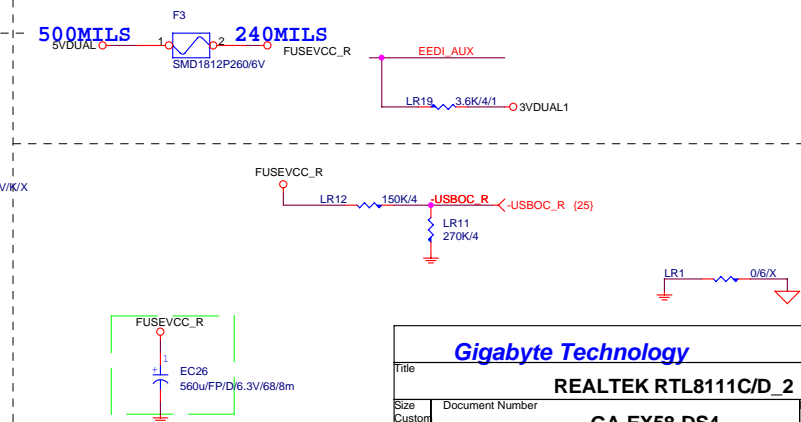
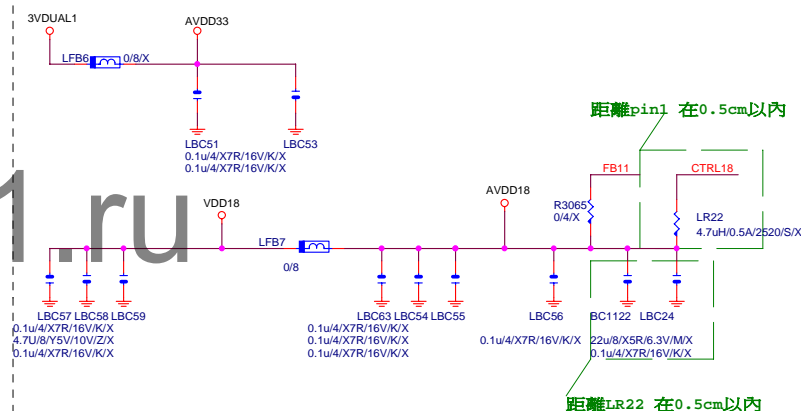
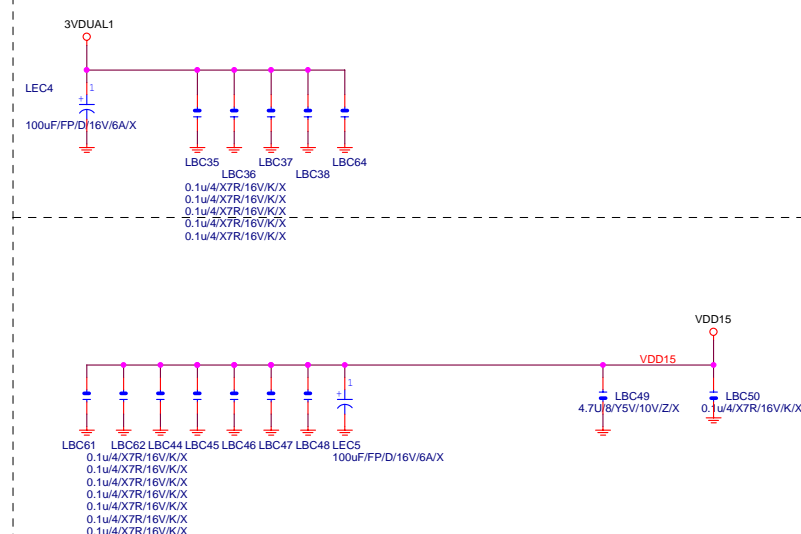
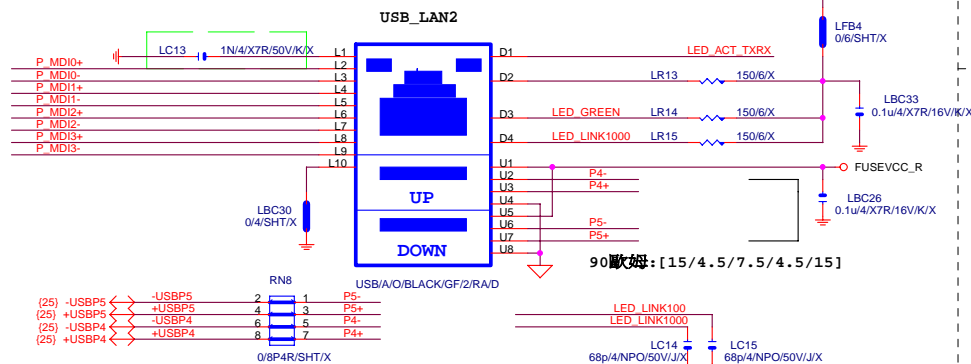
## PCIE-1G LAN



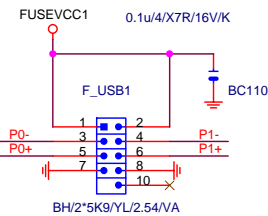
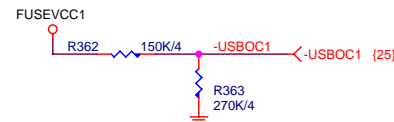
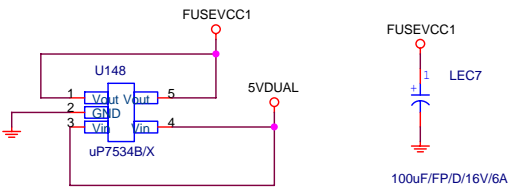
## USB\_LAN CONNECTOR

LAN 100 歐姆:[20/7/8/7/20]

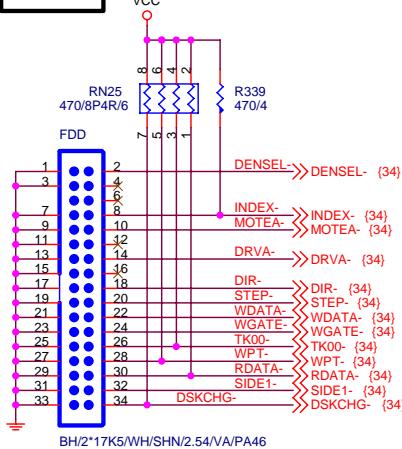
	<b>RTL8111B / RTL8101E</b>	<b>RTL8111C</b>
<b>AVDD33</b>	<b>3.3V</b>	<b>3.3V</b>
<b>AVDD18</b>	<b>1.8V</b>	<b>1.2V</b>
<b>EVDD18</b>	<b>1.8V</b>	<b>1.2V</b>
<b>VDD15</b>	<b>1.5V</b>	<b>1.2V</b>



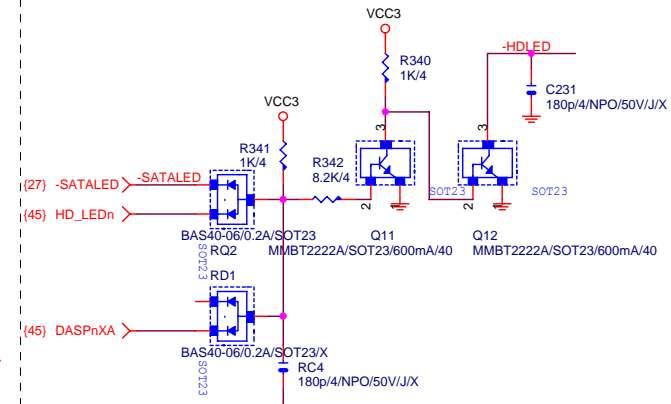
# FRONT USB1



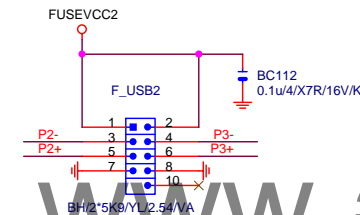
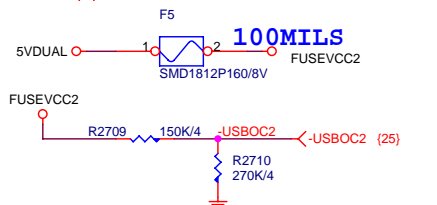
# FLOPPY



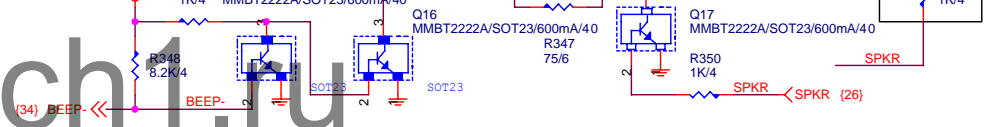
# SATA LED



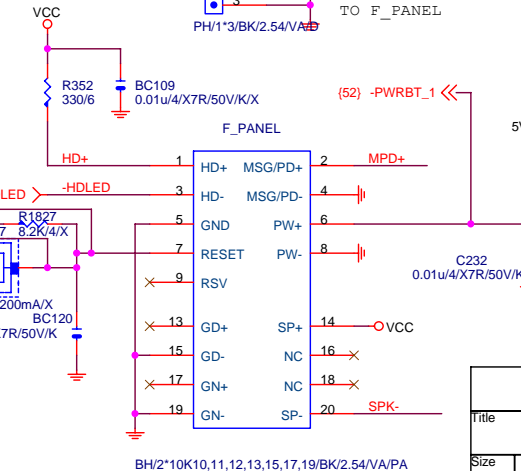
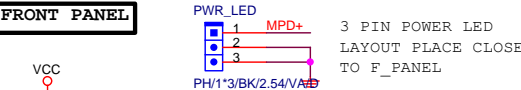
# FRONT USB2



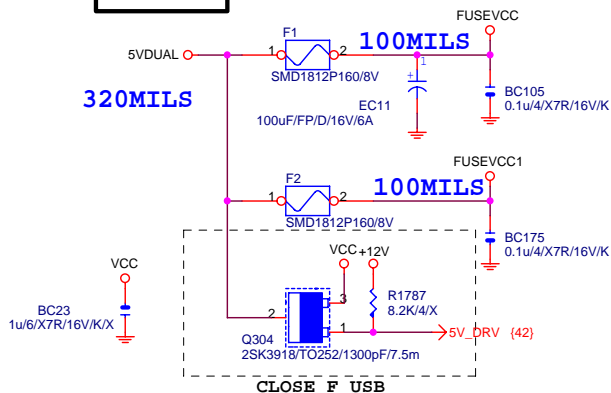
Need verify 01/15 r1.2



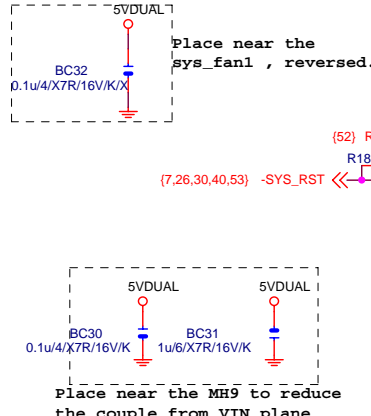
# INTEL FRONT PANEL



# USB POWER



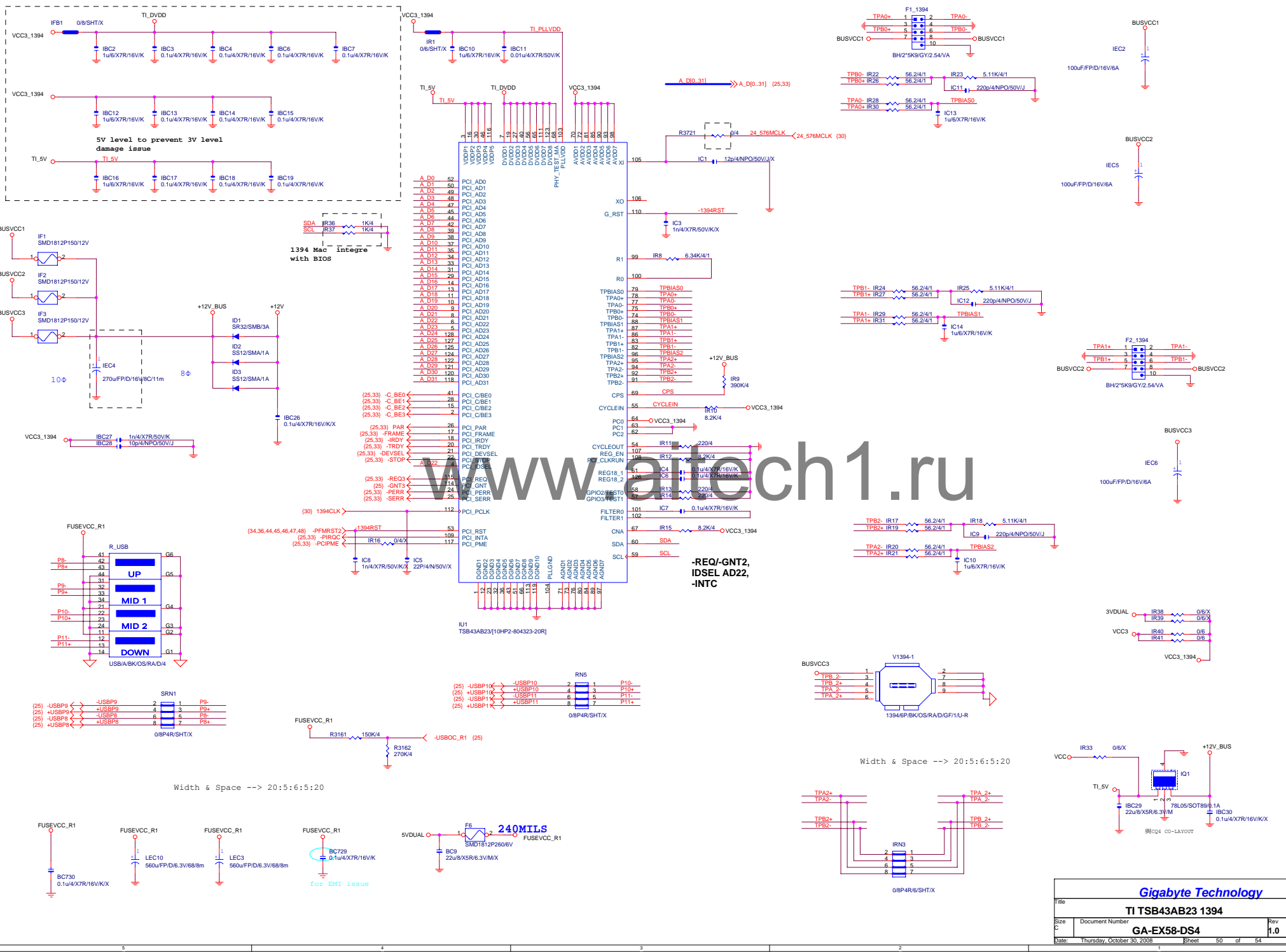
place near the sys\_fan1, reversed.



Place near the MH9 to reduce the couple from VIN plane

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FP,F_USB,USB PWR,FDD,BZ			
Title	Document Number	GA-EX58-DS4	Rev 1.0
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(34) VREF ←

(34) SYS\_TEMP ←

(34) PWM\_TEMP ←

(34) CPU\_TEMP ←

C1294  
1u/6/X7R/16V/K

C1295  
1u/6/X7R/16V/K

RS1  
10K/1/6/S

RS5  
10K/1/6/S

C1296  
5.6n/4/X7R/25V/K

R1651  
10K/4/1

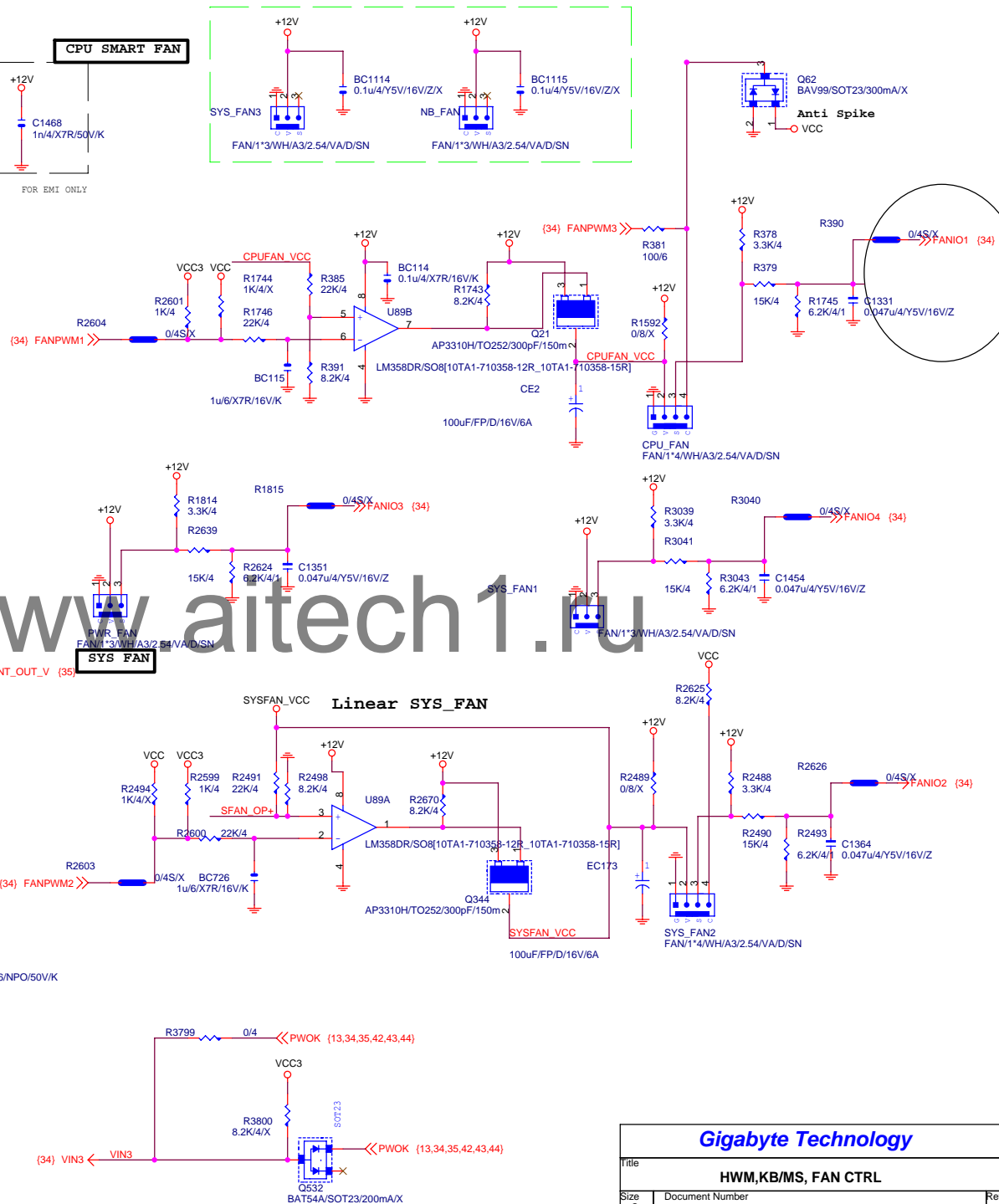
R269  
10K/4/1

R270  
30K/4/1/X

Case Open Circuits

The schematic diagram illustrates the power supply section of the ADXL345 evaluation board. A +12V input is connected to a network of resistors and capacitors. The resistors include R366 (8.2K/4), R367 (8.2K/4), R379 (8.2K/4), R368 (8.2K/4), R379B (8.2K/4), R369 (24K/4/1), R370 (10K/4/1), and R257E (8.2K/4). The capacitors include C1623 (1u6/X7R/16V/K), C1624 (1u6/X7R/16V/K), C1637 (1u6/X7R/16V/K), and C1476 (1u6/X7R/16V/K). The circuit provides various voltage levels (VCCORE, DDR15V, VCC3) and a current source (CUR) to the board's pins. The board pins are labeled (34) VIN0 through (34) VIN6.

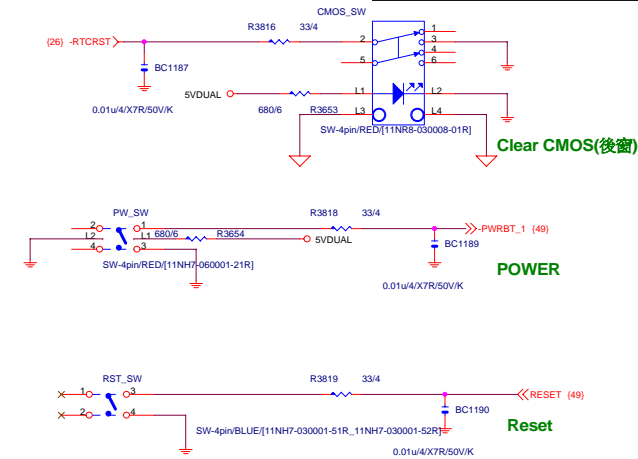
FOR EMI ONLY



Title				HWM,KB/MS, FAN CTRL			
Size	Document Number						Re
Custom	GA-EX58-DS4						1.
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## Switch 部分

Clear CMOS 90°C 料號:11NR8-030008-01R.  
Clear CMOS 180°C 料號:11NH7-060001-11R.  
Power 180°C 料號:11NH7-030001-21R.  
Reset 180°C 料號:11NH7-060001-51R.



Clear CMOS(後窗)

POWER

Reset

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UPI6262 Table

up6262	0X60-U123 (5VDUAL)	0X62-U116 (5VDUAL)	0X6A-U122 (5VDUAL)	0X6E-U124 (5VDUAL)	0X68-U115 (5VDUAL)	0X64-U117 (5VDUAL)
VREF1	CHA_ADJ	VCCA18_PLL_ADJ	VTI_ADJ	VCORE_ADJ	CHCC_ADJ	VTI_ADJ
VREF2	DDR18V_ADJ	VCCA1_1_ADJ	CHAC_ADJ	VCC15_ADJ	CHC_ADJ	VCC1_1_I_CH_ADJ
VREF3	CHB_ADJ	VCC11_ADJ	CHBC_ADJ	VCCA1_5_ADJ	MCH_RAMVREF_ADJ	VCC1_9_ADJ

